



NISE3100Series EBC573 Series

User's Manual

2008- March Edition

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Acknowledgements

The EBC573 series/NISE3100 series is a trademark of NEXCOM international CO., LTD. All other product names mentioned herein are registered trademarks of their respective owners.

Regulatory Compliance Statements

This section provides the FCC compliance statement for Class A devices and describes how to keep the system CE compliant.

Federal Communications Commission (FCC) For Class A Device

This equipment has been tested and verified to comply with the limits for a Class A digital device, pursuant to Part 15 of FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. Operation of this equipment in a residential area (domestic environment) is likely to cause harmful interference, in which case the user will be required to correct the interference (take adequate measures) at their own expense.

CE Certification

The product(s) described in this manual complies with all applicable European Union (CE) directives if it has a CE marking. For computer systems to remain CE compliant, only CE-compliant parts may be used. Maintaining CE compliance also requires proper cable and cabling techniques.

WARNINGS

Read and adhere to all warnings, cautions, and notices in this guide and the documentation supplied with the chassis, power supply, and accessory modules. If the instructions for the chassis and power supply are inconsistent with these instructions or the instructions for accessory modules, contact the supplier to find out how you can ensure that your computer meets safety and regulatory requirements.

CAUTION

Electrostatic discharge (ESD) can damage embedded computer components. Do the described procedures only at an ESD workstation. If no such station is available, you can provide some ESD protection by wearing an antistatic wrist strap and attaching it to a metal part of the computer chassis.

Safety Information

Before installing and using the EBC573/NISE3100 series, note the following precautions:

- Read all instructions carefully.
- Do not place the unit on an unstable surface, cart, or stand.
- Follow all warnings and cautions in this manual.
- When replacing parts, ensure that your service technician uses parts specified by the manufacturer.
- Avoid using the system near water, in direct sunlight, or near a hearing device.

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Chapter 1

General Information

1.1 Main Feature

Support Intel® Pentium® M/Celeron® M processor with 400MHz FSB

- Intel® 852GM chipsets with 400MHz FSB
- Dual 184-pin DIMM, support Max. 2GB DDR 200/266 memory
- Dual Intel® 82551ER 10/100 Fast Ethernet LAN
- Display interface with VGA x 1, TV-out x 1 and DVI x 1
- Internal Compact Flash x 1
- USB 2.0 x 6
- Three RS232 and one RS232/422/485 via DB44 Connector
- On board DC to DC power design with wide power input range +12VDC ~ +30VDC

1.2 Specifications of EBC573

Items	Description
CPU Support	<ul style="list-style-type: none">• Support Intel® Pentium® M processor in 478-pin, Micro-FCPGA package (mPGA479M Socket) from 1.6 to 1.8 GHz with 1 MB or 2 MB L2 Cache• Support Intel® Celeron® M processor in 478-pin, Micro-FCPGA package (mPGA479M Socket) from 1.3 to 1.5 GHz with 512 KB or 1 MB L2 cache• Supports Low Voltage and Ultra Low Voltage Intel® Pentium® M or Intel® Celeron® M processors in 479-ball, Micro-FCBGA packages (Option, with Minimum order Q'ty requirement)
Chipset	<ul style="list-style-type: none">• Intel® 852GM• Intel® 82801DB x 1 I/O controller Hub (ICH4)
Memory	<ul style="list-style-type: none">• 184-pin DDR DIMM socket x 2, support un-buffered, non-ECC DDR 200/266 SDRAM up to 2 GB
BIOS	<ul style="list-style-type: none">• Plug & Play support• Award System BIOS• 4M bits flash ROM• ACPI 1.0b compliant

Items	Description
LAN	<ul style="list-style-type: none"> ● Intel® 82551QM 10/100 Fast Ethernet LAN x 2, support PXE LAN boot
Display	<ul style="list-style-type: none"> ● Intel® 852GM chipset integrated with graphics controller, up to 64MB of dynamic video memory allocation ● Analogue VGA interface: DB15 VGA connector x 1, resolution support up to 1600 x 1200 @85Hz and up to 2048 x 1536 @ 75Hz ● DVI interface: 34-pin box header x 1 for DVI 12bit interface, resolution support up to 1600 x 1200 ● TV-out interface: 7-pin mini-DIN x1 for S-video TV-out interface, resolution support up to 1024 x 768. (On board Chrontel CH7009B DVI/TV out Transmitter x1) ● LVDS interface: DB44-pin connector x 1, support 18/24-bit single pixel or 18-bit dual pixel LVDS panel (resolution support up to 1400 x 1050)
I/O Interface	<ul style="list-style-type: none"> ● 1 x S-Video TV-out ● 1 x PS/2 keyboard/mouse ● 1 x DB15 VGA ● 6 x USB2.0 <ul style="list-style-type: none"> ■ Rear: 4 ports ■ Front: 2 ports in front, either edge USB connector or 6-pin 2.0mm JST connector. ● 2 x 10/100 Fast Ethernet RJ45 LAN ports ● 4 x Serial port from one DB44 pin connector via one external cable, 16C550 UARTs (RS232 x 3 and RS232/422/485 x 1) <ul style="list-style-type: none"> ■ COM2 supports RS 232/422/485 selected by 1x 10 switch ■ COM4 pin-9 reserved for 12V or 5V output (set by jumper) ● 1 x Audio Mic-in and 1x Speaker-out (2W amplified,) AC'97 codec ● 1 x 44-pin HDD connector x 1, Ultra ATA33 support (Secondary) ● 1 x Internal Compact Flash socket, support One Type II Compace Flash card (Primary) ● 1 x Parallel port with 2x13 box header, bi-directional, EPP/ECP support ● 1 x FDD box header, supports standard 3.5" floppy drive ● GPIO: 4-bit TTL input, 4-bit TTL output (TTL: level 0-5V) ● On-board buzzer x 1 ● 1 x on-board pin header for IrDA Tx-Rx ● On-board pin header for Power/HDD Access LED and Reset
Expansion	Standard 32-bit PCI slot x 1
System Monitor	Monitor system Voltage, 3 x temperature, 3 x fan speed

Real Time Clock	<ul style="list-style-type: none"> ● On-chip RTC with battery back up ● External Lithium battery x 1
Watchdog Timer	Software selectable time-out interval (1~128 sec. 1~127 min.)
Dimensions	<ul style="list-style-type: none"> ● 260mm (L) x 180mm (W)
Power Requirements	<ul style="list-style-type: none"> ● On board DC to DC power design ● Power input: +12VDC to +30VDC input via Mini-DIN 4-pin power connector ● Power mode: support AT and ATX mode (from board version: D)
Environments	<ul style="list-style-type: none"> ● Board-level operating temperatures: 0°C to 60°C ● Storage temperatures: -20°C to 80°C ● Relative humidity: 10% to 90% (Non-condensing)
Certification	<ul style="list-style-type: none"> - CE - FCC A

1.3 Specifications of NISE3100 series

Items	Description
Main Board	<ul style="list-style-type: none"> ● EBC573FL (LF)-AX ● Support Intel® Pentium® M processor in 478-pin, Micro-FCPGA package (mPGA479M Socket) from 1.6 to 1.8 GHz with 1 MB or 2 MB L2 Cache ● Support Intel® Celeron® M processor in 478-pin, Micro-FCPGA package (mPGA479M Socket) from 1.3 to 1.5 GHz with 512 KB or 1 MB L2 cache ● Supports Low Voltage and Ultra Low Voltage Intel® Pentium® M or Intel® Celeron® M processors in 479-ball, Micro-FCBGA packages (Option, with Minimum order Q'ty requirement) ● Chipset: Intel® 852GM and Intel® 82801DB x 1 I/O controller Hub (ICH4)
Memory	<ul style="list-style-type: none"> ● 184-pin DDR DIMM socket x 2, support un-buffered, non-ECC DDR 200/266 SDRAM up to 2 GB
I/O Interface - Front	<ul style="list-style-type: none"> ● Customized logo (Optional) ● Power/HDD Access/LAN status LEDs ● 2x USB 2.0 ● ATX power on/off switch
I/O Interface - Rear	<ul style="list-style-type: none"> ● 1 x S-Video TV-out ● 1 x PS/2 keyboard/mouse ● 1 x DB15 VGA ● 4 x USB2.0 ● 2 x 10/100 Fast Ethernet LAN ● 1 x DVI interface ● 4 x Serial port from one DB44 pin connector via one external cable, 16C550 UARTs (RS232 x 3 and RS232/422/485 x 1) <ul style="list-style-type: none"> ■ COM2 supports RS 232/422/485 selected by 1x 10 switch ■ COM4 pin-9 reserved for 12V or 5V output (set by jumper) ● 1 x Audio Mic-in and 1x Speaker-out (2W amplified) AC'97 codec ● 1 x 2-pin connector output for remote power on/off switch ● 1 x Parallel Port (NISE3150 only)
Device	<ul style="list-style-type: none"> ● 1 x internal Compact Flash socket ● 1 x Internal 2.5" HDD driver bay ● 1 x Slim DVD-ROM/CD-R/RW Combo (8D/24W/24E/24R, Slave) (for NISE3150 only)
Expansion	<ul style="list-style-type: none"> ● Standard 32-bit/33MHz PCI slot x 1 (for NISE3100) <ul style="list-style-type: none"> ■ PCI length support: 160mm (with 2.5" HDD installed) ■ PCI length support: 240mm (without 2.5" HDD installed)

	<ul style="list-style-type: none"> ● Standard 32-bit/33MHz PCI slot x 2 (for NISE3100P2 only) <ul style="list-style-type: none"> ■ PCI length support: 160mm x1 and 240mm x1 (with 2.5" HDD installed) ■ PCI length support: 240mm x2 (without 2.5" HDD installed) ● DC to DC power designed, support range from 12V to 30V DC (Max: 120 Watts)
Power Input	<ul style="list-style-type: none"> ● 1 x External 120W AC adapter <ul style="list-style-type: none"> ■ Power input: 100 to 240V AC 2A 50/60Hz ■ Power output: 19V DC
Dimensions	195 mm (W) x 268 mm (D) x 80 mm (H)
Construction	Aluminum chassis with fan-less design
Environment	<ul style="list-style-type: none"> ● Operating temperature: <ul style="list-style-type: none"> ■ Ambient with air flow : 0°C to 40°C ■ NISE 3100 Tcase (Surface Temperature of Chassis) <ul style="list-style-type: none"> ◆ 5°C to 40°C (W/HDD) ◆ -10°C to 45°C (W/CF card only) ● Storage temperature: -20°C to 80°C ● Relative humidity: 10% to 90% (Non-condensing)
Certification	<ul style="list-style-type: none"> ● CE ● FCC A

Ordering Information

EBC573FL (LF)-AX

Socket 478 support Intel® Pentium®-M processor with VGA/LAN x 2/USB2.0 x6/TV-out/DVI/Audio/COM port x 4

EBC573FL (LF) (P/N: 10E00057301X0)

On-board Intel® Celeron M 600MHz Zero L2 Cache VGA/LAN x 2/USB2.0 x6/TV-out/DVI/Audio/COM port x 4

Power Cord (P/N: 60233POW38X00)

Power cable with 3-pin MICKY mouse head (US)

Power Cable (P/N: 60233POW39X00)

Power cable with 3-pin MICKY mouse head (Euro)

Power Cable (P/N: 60233POW40X00)

Power cable with 3-pin MICKY mouse head (UK)

120W AC/DC Adapter with Keylock (P/N: 7410120002X0)

Power Input: 100~240Vac 2A 50/60HZ, Power Output: 19 Vdc, 6.32A

Ordering Information

NISE3100 (LF) (PN: 10J00310000X0)

Support Intel Pentium M/Celeron M fan-less bare-bone system with one PCI expansion slot

NISE3100 (LF)-C65 (PN: 10J00310001X0)

Intel Celeron M 600 MHz BGA CPU with 512KB L2 fan-less system with one PCI expansion slot

NISE3100 (LF)-160 (PN: 10J00310006X0)

Intel Pentium M 1.6GHz CPU fan-less system with one PCI expansion slot

NISE3100 (LF)-320 (PN: 10J00310004X0)

Intel Celeron M 1.3GHz CPU fan-less system with one PCI expansion slot

NISE3100 (LF)-370 (PN: 10J00310008X0)

Intel Celeron M 1.5GHz CPU fan-less system with one PCI expansion slot

NISE3100 (LF)-373 (PN: 10J00310015X0)

ULV Intel Celeron M 1GHz, 512KB L2 Cache, CPU fan-less system with one PCI expansion slot

NISE3100P2 (PN: 10J3100P201X0)

Support Intel Pentium M/Celeron M fan-less bare-bone system with two PCI expansion slot

NISE3150 (PN: 10J00315000X0)

Support Intel Pentium M/Celeron M fan-less bare-bone system with 1x Slim DVD-ROM/CD-R/RW Combo (8D/24W/24E/24R, Slave) and one Parallel port

NISE3100AT (PN: 10J00310014X0)

Support Intel Pentium M/Celeron M fan-less bare-bone system with one PCI expansion slot and **one LVDS connector (Power mode: AT only)**

NISE3100P2AT (PN: 10J3100P204X0)

Support Intel Pentium M/Celeron M fan-less bare-bone system with two PCI expansion slot and **one LVDS connector (Power mode: AT only)**

■ EBC573:

Test Configuration:

In Date: 10/19/2004	Out Date: 10/19/2004
BIOS Version: C-005	
Configuration	Model Name
CPU Type/Speed/Model/Micro-Code	Intel Pentium-M 1.6GHz
Memory Type/Speed/Size/On-Board Chip/Vendor	DDR/DDR333/1GB*2/SAMSUNG
Power Supply Type/Model/Watts-Value	AT/Chroma 6210K-40
PS2 Keyboard and Mouse	Logitech Y-SJ17 Logitech M-S34
HDD Drive	WD WD400
DVD-ROM Devices	MSI C52 (MS-8152)
Floppy Drive	TEAC FD-235HF
Monitor	ViewSonic E70
Operation System	Windows2000 (English)-5.00.2195+SP4
Test Software / Program	HCT 9.5

Test Criteria:

1. Test configuration should include HDD drive, CD-ROM, FDD device.
2. Full loading mode should utilize CPU 100% with run HCT test program.
3. Light loading mode will utilize CPU loading below 5%, and there is no data or application running.

Test Result:

DC in 12V~30V: (System-Only)	CPU Type: Intel Pentium-M 1.6GHz	
	12V	30V
Full-Loading Mode (A)	3.1	1.3
Light-Loading Mode (A)	2.2	1.0
Standby Mode (HDD Drive Power-Down) (A)	1.4	0.7

Power supply must consumed watts and currents

Power Type	+12V	12V to +5V	Total Watt
Consumed watts	48W	72.27W	120.27
Consumed currents (Item A)	4	14.45A	120.27
Actually required currents (Item A/0.8)	5	18	150

Key component power consumption (Unit : A)

Chipset	Vcore 0.7~1.7	Vccp 1.05	1.35V	+1.25V	+1.5V	+1.8V	+2.5V	+3.3V	+5V	+12v
Pentium M	25					0.5				
INTEL 82852GM		2.4	1.8	0.01	0.32		0.05			
DIMM X2				1.5			8			
INTEL ICH4 (82801B)					0.5			0.65		
CLOCK GEN. (CK-408)								0.28		
INTEL 82551ER								0.31		
CH7009B					0.004			0.3		
Super IO (ITE8712)									1	
Super IO (ITE8710)								0.6		
Other								2		4
Total consumption	25	2.4	1.8	1.51	0.824	0.5	8.05	4.14	1	4
Total Watt (Unit: W)	24.5	2.52	2.43	1.89	1.24	0.9	20.13	13.66	5	48
Transfer Voltage (Unit: V)	12 to 5	12 to 5	12 to 5	12 to 5	12 to 5	12 to 5	12 to 5	12 to 5	12 to 5	12

■ NISE3100 system:

Test Configuration:

Configuration	Model Name
CPU Type/Speed/Model/Micro-Code	Intel Pentium-M, 1.6GHz, 400MHz FSB
Memory Type/Speed/Size/On-Board Chip/Vendor	DDR/DDR333/512MB Apacer
Power Supply Type/Model/Watts-Value	19V, 120W power adapter with PSE mark
PS2 Keyboard and Mouse	Keyboard: Lemel KB-5201-P Mouse: Genivs Easy Mouse PS/2
HDD Drive	Hitachi IC25N020ATCD04-0 20G
CPU Heat Sink	107 x 84 x 29mm AL, for NISE3100
System FAN	N/A
Operation System	Windows 2000

Test Result:

DC in 12V~30V: (System-Only)	CPU Type: Intel Pentium-M 1.6GHZ
	19V
Full-Loading Mode (A)	1.6A, 30.4W
Standby Mode (HDD Drive Power-Down) (A)	0.6A, 11.4W

1.5 Board Layout

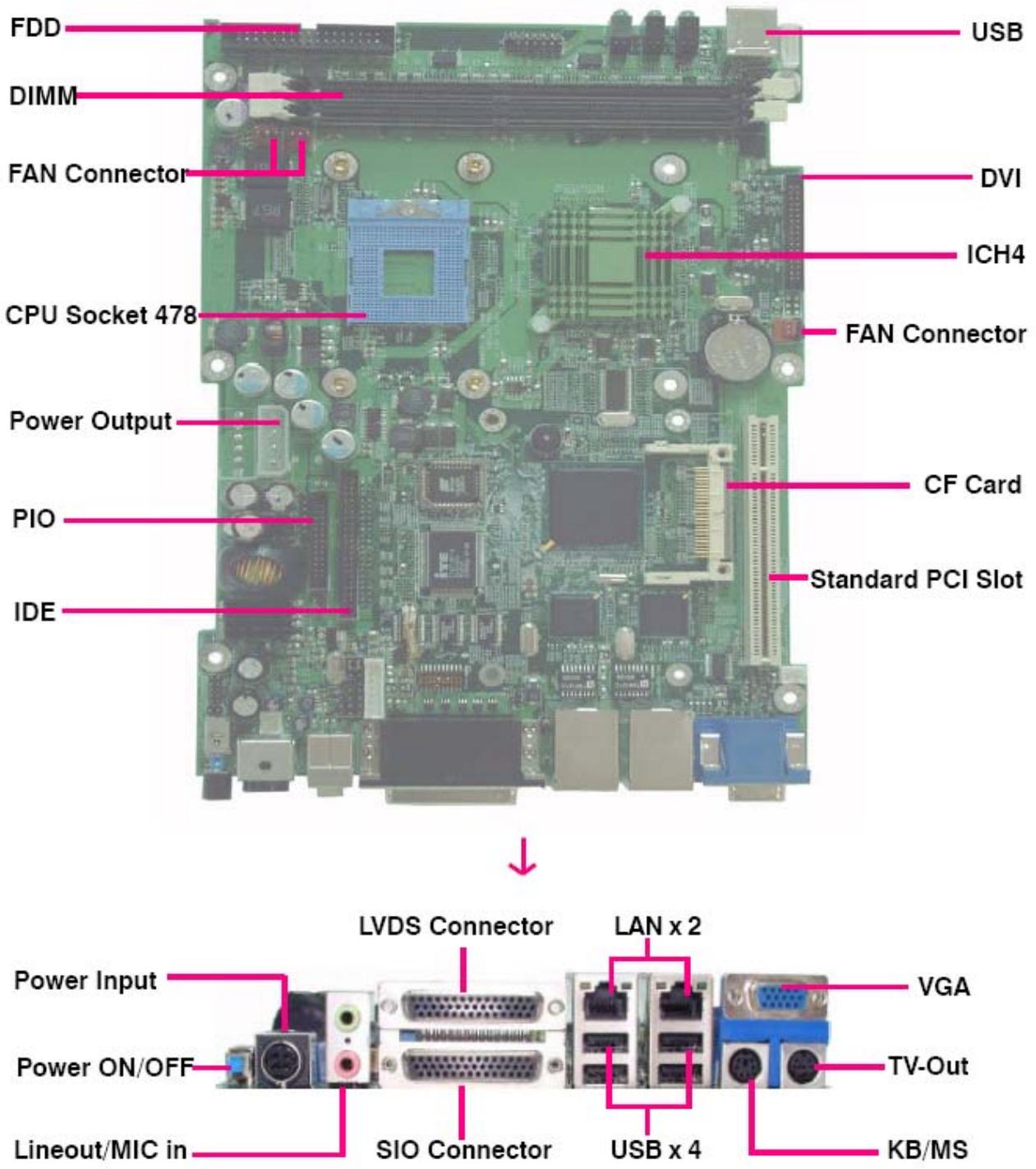


Figure 1.1: Birdeye's View of the EBC573

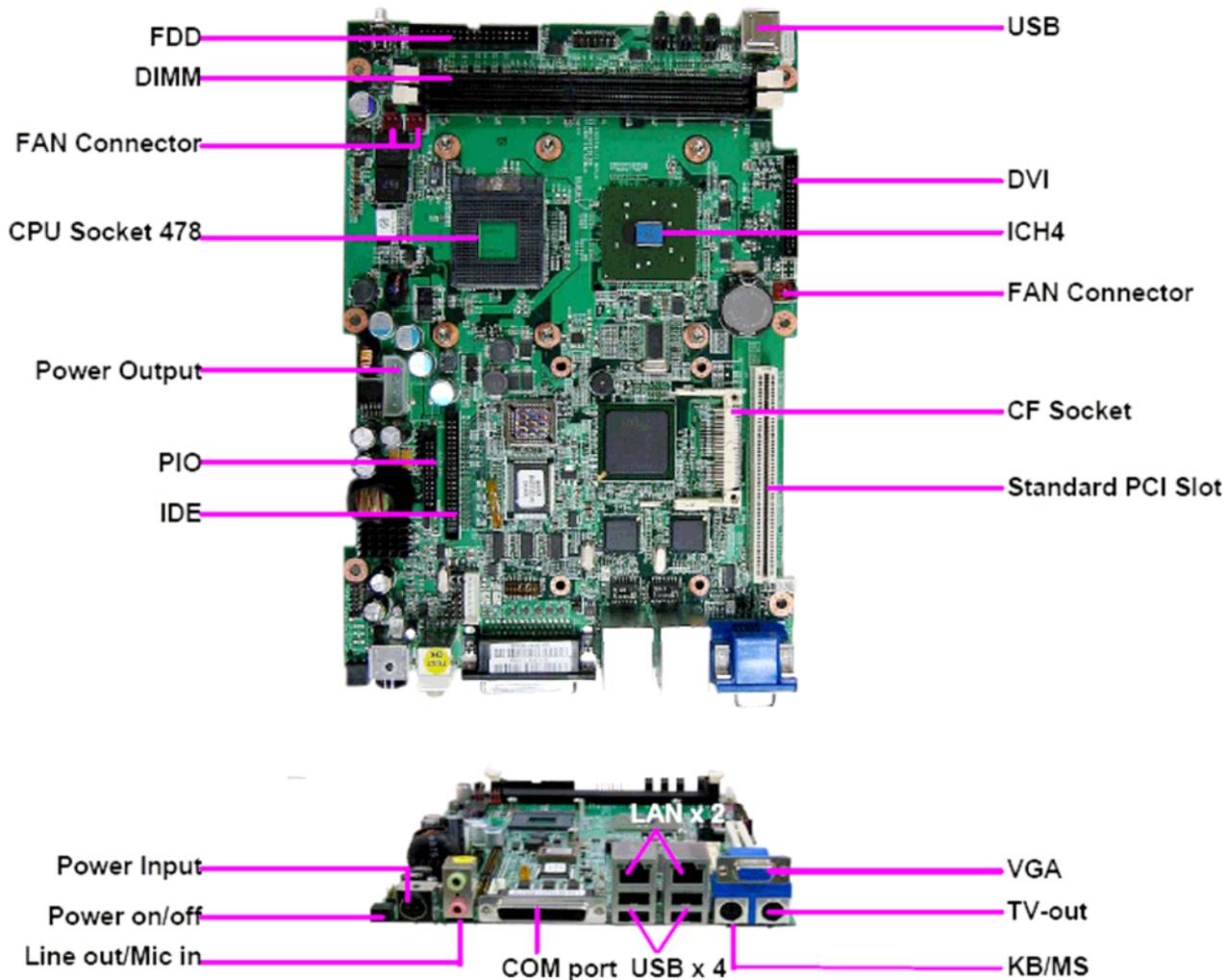


Figure 1.2: Birdseye's View of the EBC573 (LF)-AX

1.6 Dimensions

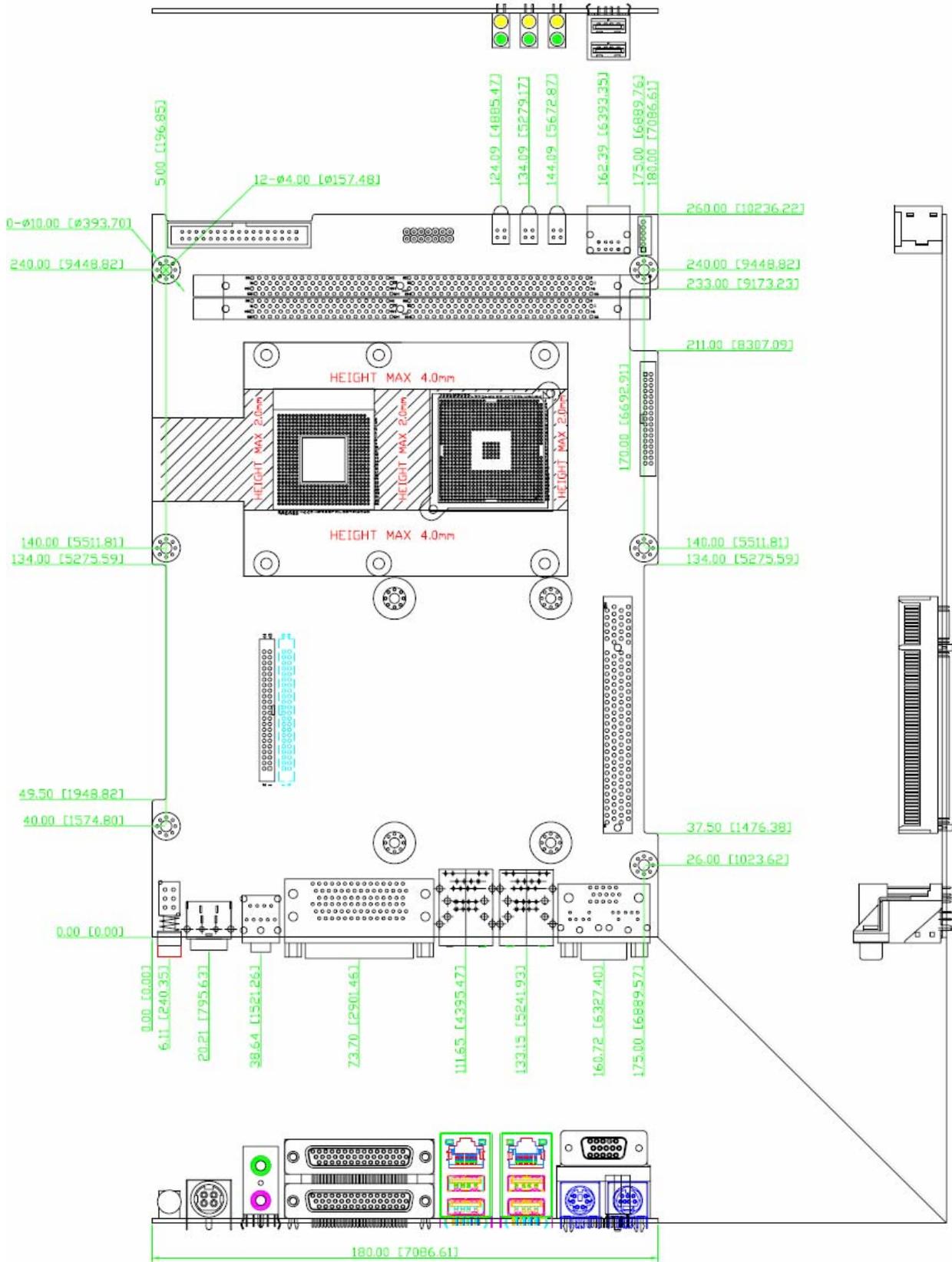


Figure 1.3: Mechanical Drawing of the EBC573

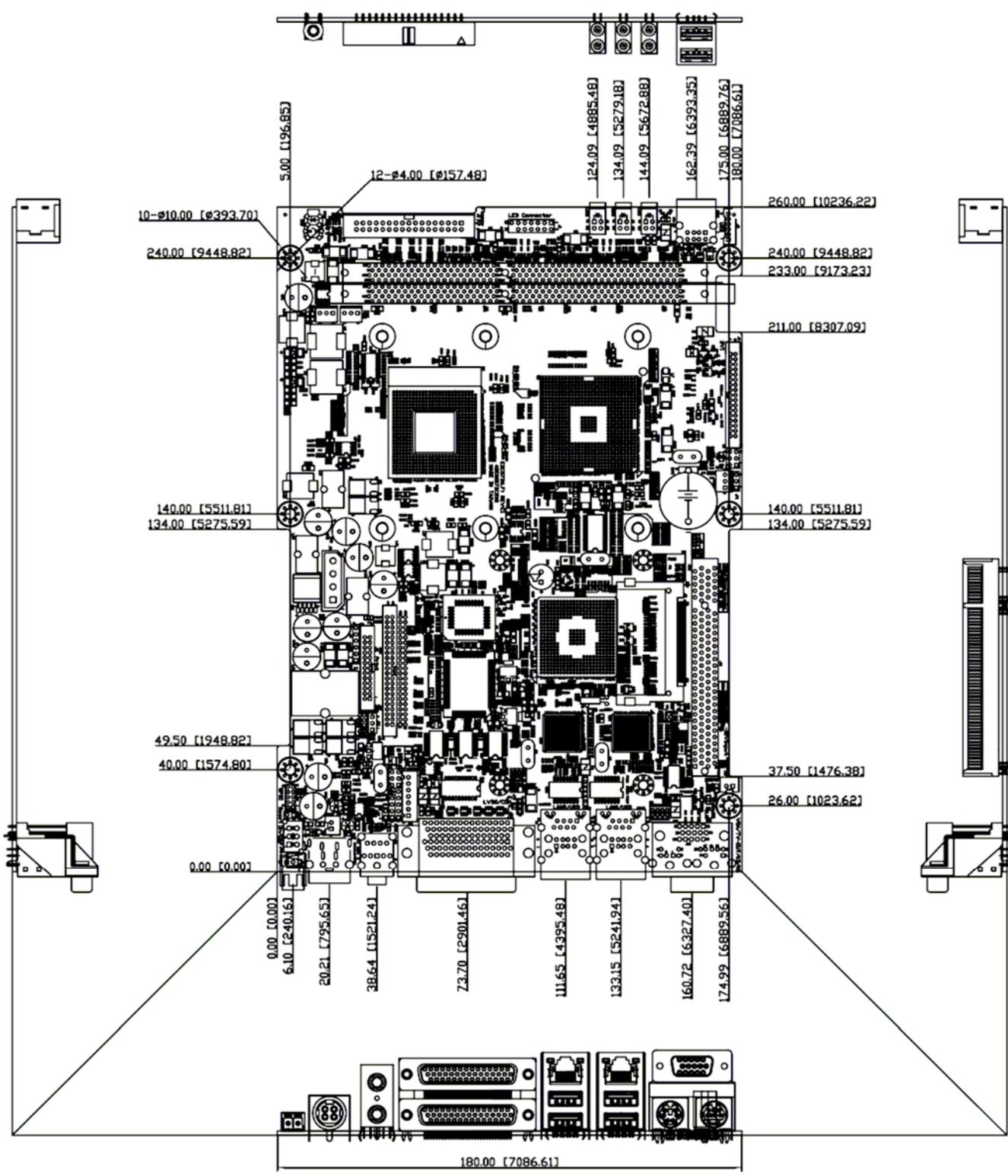


Figure 1.4: Birdeye's View of the EBC573 (LF)-AX

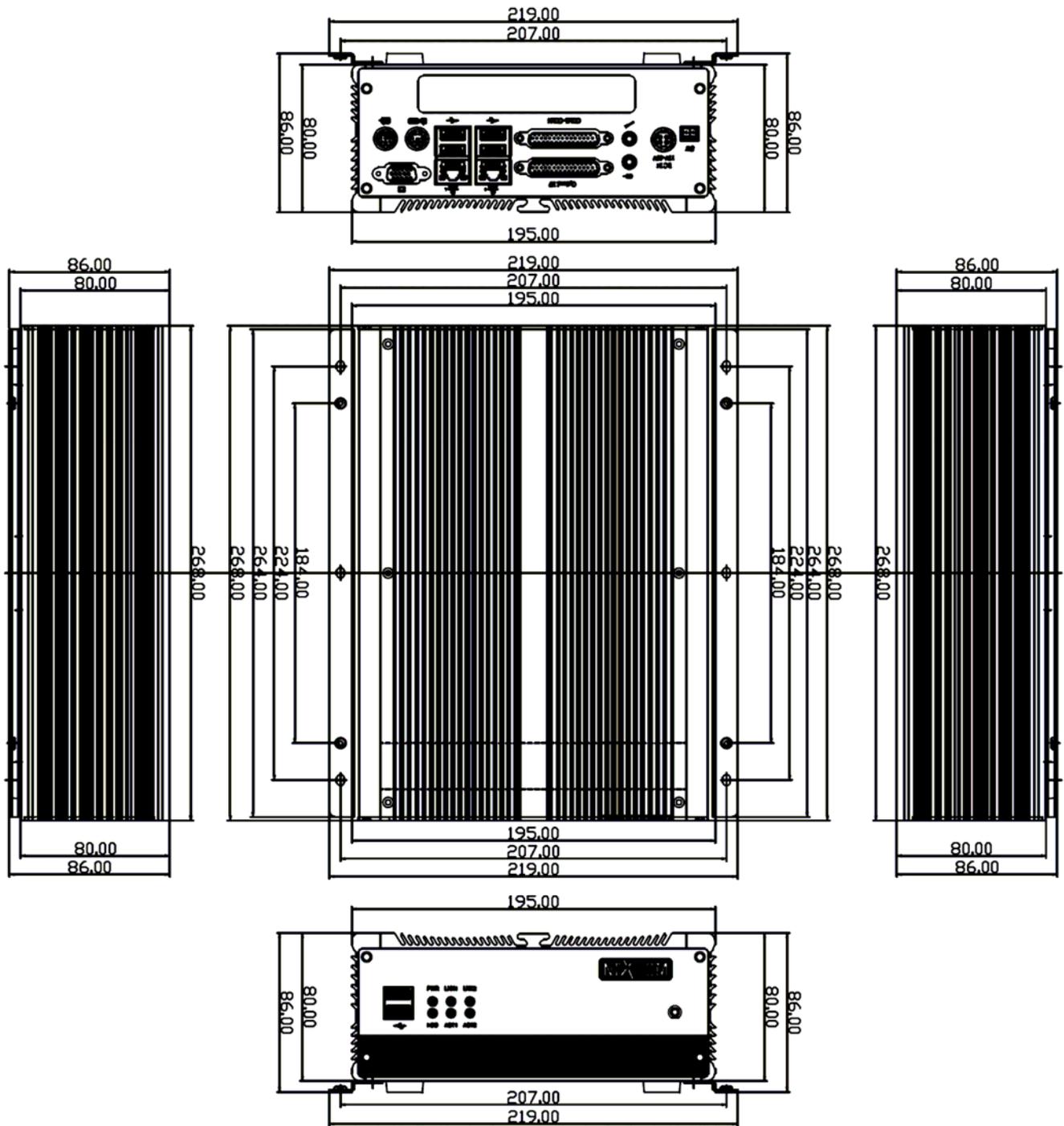


Figure 1.5: Birdeye's View of the NISE3100

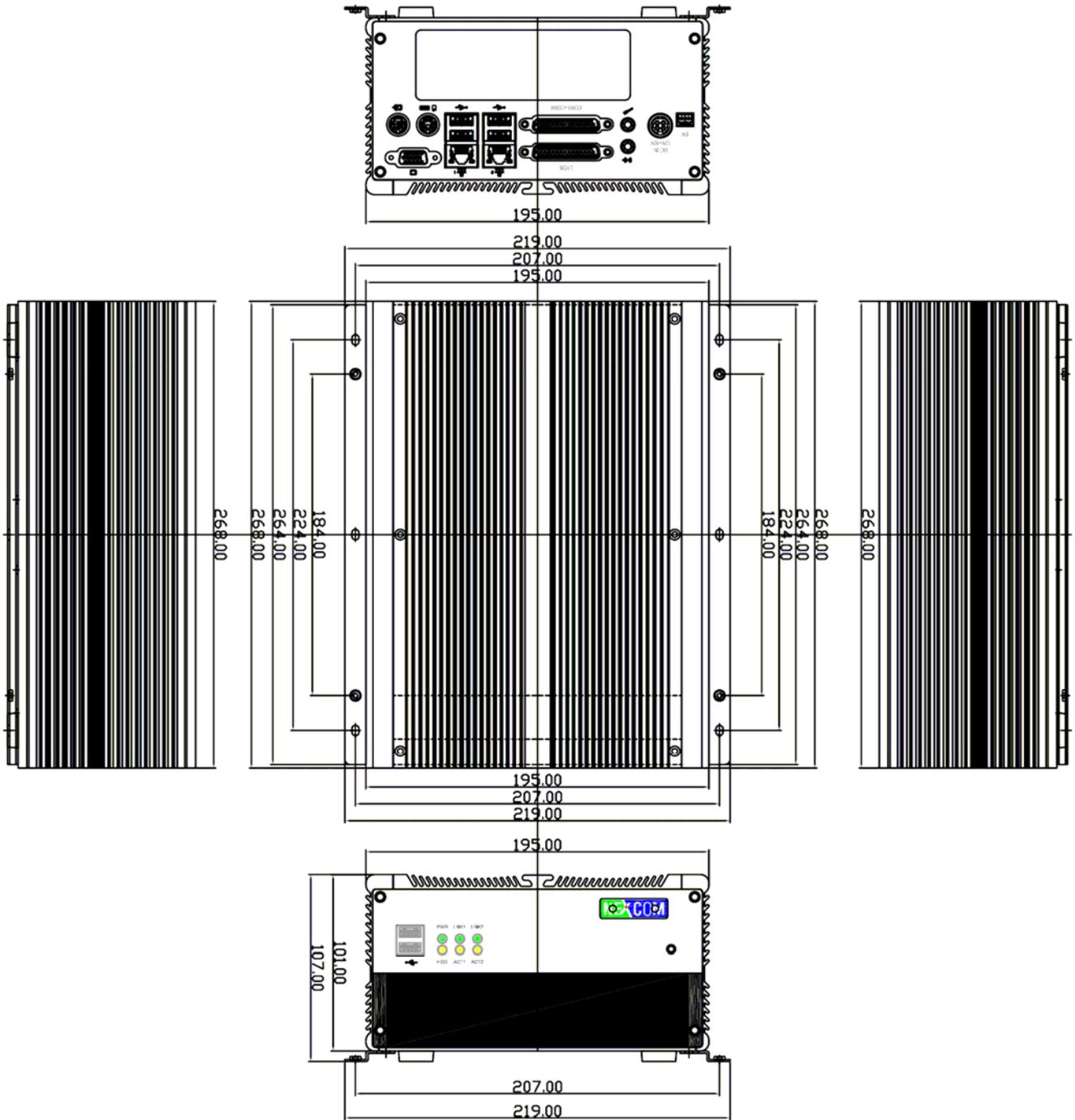


Figure 1.6: Birdeye's View of the NISE3100P2

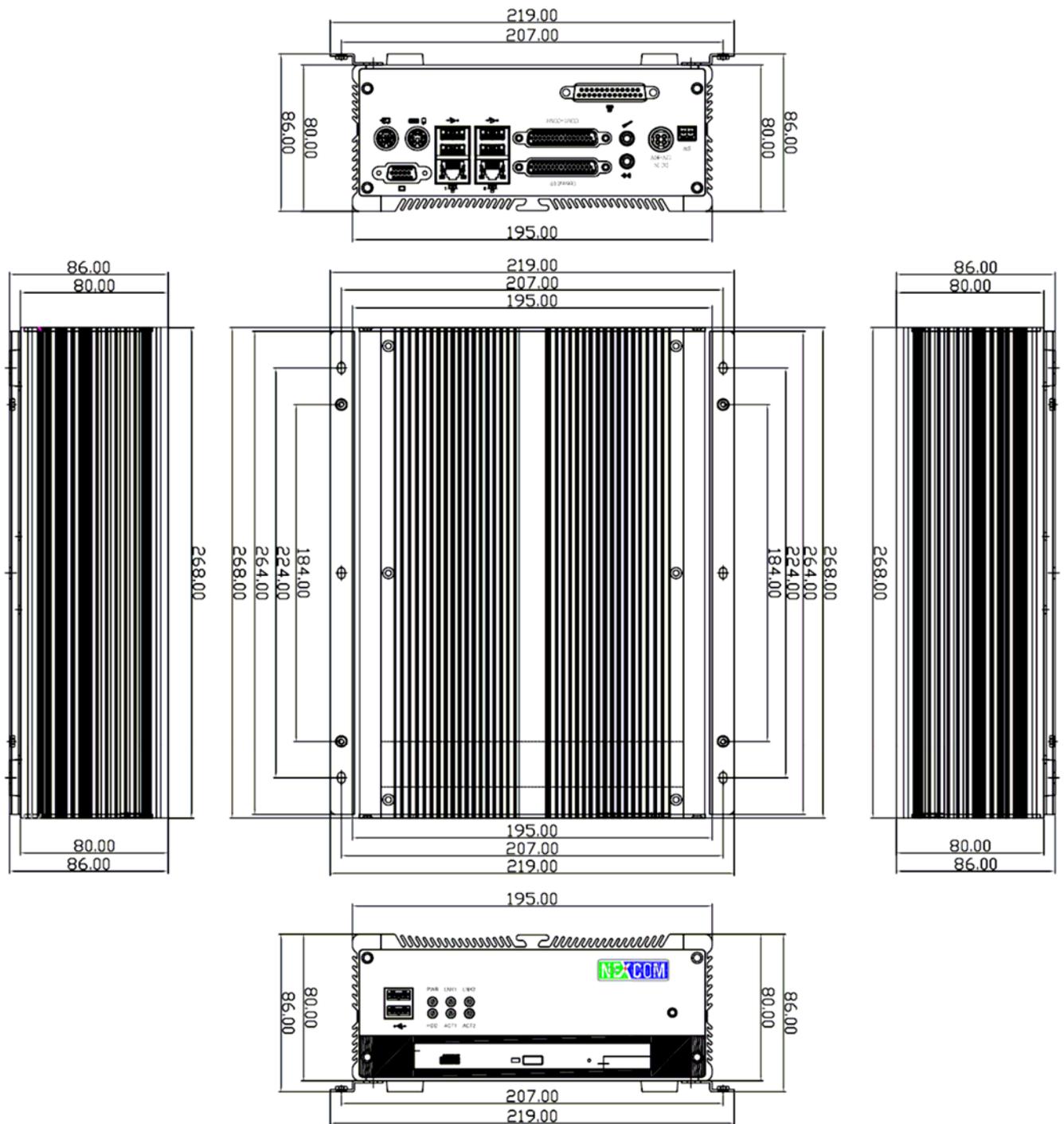


Figure 1.7: Birdeye's View of the NISE3150

Chapter 2

Jumper Setting

This chapter of the User's Manual describes how to set jumpers.

Note: The procedures that follow are generic for all NISE3100/EBC573 series.

2.1 Before You Begin

Ensure you have a stable, clean working environment. Dust and dirt can get into components and cause a malfunction. Use containers to keep small components separated.

Adequate lighting and proper tools can prevent you from accidentally damaging the internal components. Most of the procedures that follow require only a few simple tools, including the following:

- ◆ A Philips screwdriver
- ◆ A flat-tipped screwdriver
- ◆ A set of jewelers Screwdrivers
- ◆ A grounding strap
- ◆ An anti-static pad

Using your fingers can disconnect most of the connections. It is recommended that you do not use needle-nosed pliers to disconnect connections as these can damage the soft metal or plastic parts of the connectors.

Before working on internal components, make sure that the power is off. Ground yourself before touching any internal components, by touching a metal object. Static electricity can damage many of the electronic components. Humid environment tend to have less static electricity than dry environments. A grounding strap is warranted whenever danger of static electricity exists.

2.2 Precautions

Computer components and electronic circuit boards can be damaged by discharges of static electricity. Working on the computers that are still connected to a power supply can be extremely dangerous. Follow the guidelines below to avoid damage to your computer or yourself:

- ◆ Always disconnect the unit from the power outlet whenever you are working inside the case.
- ◆ If possible, wear a grounded wrist strap when you are working inside the computer case. Alternatively, discharge any static electricity by touching the bare metal chassis of the unit case, or the bare metal body of any other grounded appliance.
- ◆ Hold electronic circuit boards (such as the EBC573 board) by the edges only. Do not touch the components on the board unless it is necessary to do so. Don't flex or stress the circuit board.
- ◆ Leave all components inside the static-proof packaging that they shipped with until they are ready for installation.
- ◆ Use correct screws and do not over tighten screws.

2.3 Setting Jumpers

A jumper is the simplest kind of electric switch. It consists of two metal pins and a cap. When setting the jumpers, ensure that the jumper caps are placed on the correct pins. When the jumper cap is placed on both pins, the jumper is **SHORT**. If you remove the jumper cap, or place the jumper cap on just one pin, the jumper is **OPEN**. Please see the following illustrations

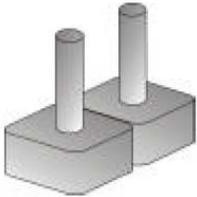
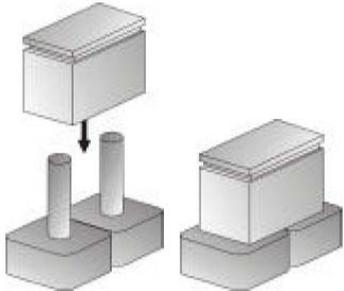
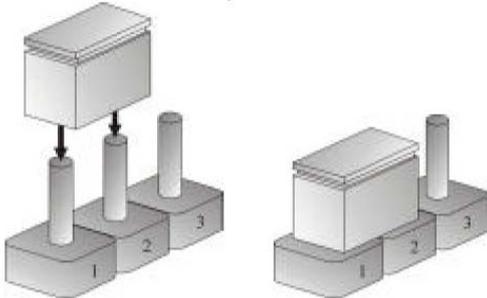
<p>The illustrations on the right show a 2-pin jumper. When the jumper cap is placed on both pins, the jumper is SHORT. If you remove the jumper cap, or place the jumper cap on just one pin, the jumper is OPEN.</p>		
	<p>Open (Off)</p>	<p>Short (On)</p>
<p>These illustrations show a 3-pin jumper. Pins 1 and 2 are SHORT.</p>		

Table 2-1: Setting Jumpers

2.4 Location of Jumpers

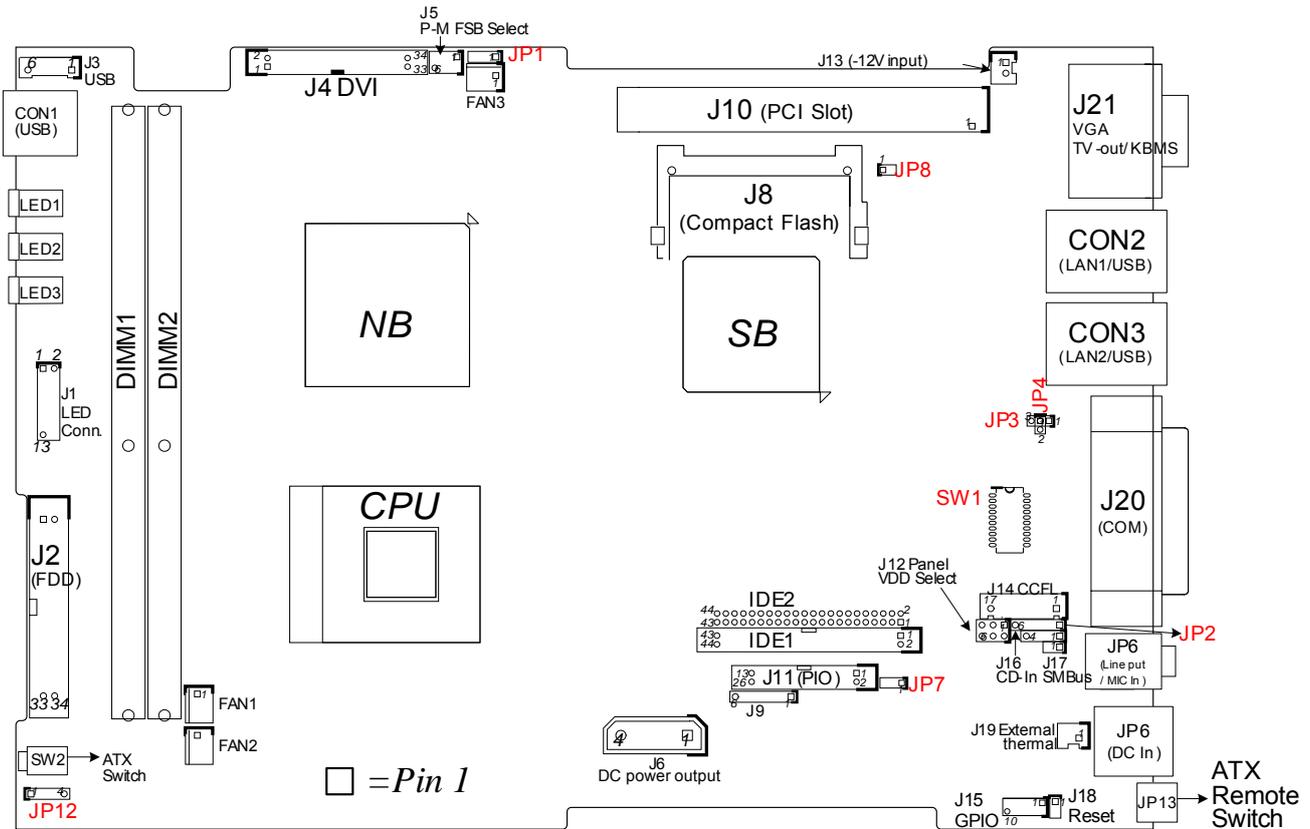


Figure 2-1: Jumper Location

2.5 Function of Jumper

Pin	Function	Pin	Function
JP1	CMOS Status Select	JP7	DC Adapter Input Voltage Select
JP3,JP4	COM4 RI# or Power Status Select	J12	PANEL VDD Select
J5	Pentium M FSB Select	SW1	COM2 RS232/422/485 Select

2.6 Pin Definition

1. IDE connector (IDE1, IDE2)

A. Connector size: 2 x 22 = 44 Pins BOX Header, (2.0 mm Pitch)

B. Connector location:



C. Connector pin definition

Pin	Definition	Pin	Definition
1	RSTDRV-	2	GND
3	IDED7	4	IDED8
5	IDED6	6	IDED9
7	IDED5	8	IDED10
9	IDED4	10	IDED11
11	IDED3	12	IDED12
13	IDED2	14	IDED13
15	IDED1	16	IDED14
17	IDED0	18	IDED15
19	GND	20	NC
21	IDEREQ-	22	GND
23	IDEIOW-	24	GND
25	IDEIOR-	26	GND
27	IDERDY	28	IDE-PD1
29	IDEACK-	30	GND
31	IDEIPQ	32	NC
33	DA1	34	66 DETECT
35	DA0	36	DA2
37	SCS1	38	SCS3
39	IDEACT-	40	GND
41	VCC5	42	VCC
43	GND	44	NC

2. SIO connector (J20A)

- A. Connector size: 3 X 15 = 44 Pin
- B. 44 PIN D-SUB FEMALE



- C. Connector pin definition (* COM 2 for RS232/422/485)

	Pin	Definition	Pin	Definition
COM1	1	DCD	2	RXD
	3	TXD	4	DTR
	5	GND	6	DSR
	7	RTS	8	CST
	9	RI	10	GND
COM2 (RS232)	11	DCD#: Data Carrier Detect.	12	RXD#: Receive Data
	13	TXD: Transmit Data	14	DTR#: Data Terminal Ready
	15	GND	16	DSR#: Data Set Ready
	17	RTS#: Request To Send	18	CTS#: Clear To Send
	19	RI#: Ring Indicator (Could be a 5V or 12V Power pin)	20	GND
COM3	21	DCD	22	RXD
	23	TXD	24	DTR
	25	GND	26	DSR
	27	RTS	28	CST
	29	RI	30	GND
COM4	31	DCD	32	RXD
	33	TXD	34	DTR
	35	GND	36	DSR
	37	RTS	38	CST
	39	RI	40	GND
GPIO	41	GP27IN	42	GP26IN
	43	GP23OUT	44	GP22OUT

COM2 (RS422)	11	TXD-: Transmit Data Negative	12	TXD+: Transmit Data Positive
	13	RXD+: Receive Data Positive	14	RXD-: Receive Data Negative
	15	GND.DCD#: Data Carrier Detect	16	RTS-: Request To Send Negative
	17	RTS#: Request To Send Positive	18	CTS+: Clear To Send Positive
	19	CTS-: Clear To Send Negative (Could be a 5V or 12V Power pin)	20	GND
COM2 (RS485)	11	TXD-: Transmit Data Negative RXD-: Receive Data Negative	12	TXD+: Transmit Data Positive RXD+: Receive Data Positive
	13	Reserved	14	Reserved
	15	Reserved	16	Reserved
	17	Reserved	18	Reserved
	19	Reserved (Could be a 5V or 12V Power pin)	20	GND

3. PIO connector (J11)

A. Connector size: 2 X 13 = 25 Pin BOX Header (2.0 mm Pitch)

B. Connector location

14 26

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C. Connector pin definition

Pin	Definition	Pin	Definition
1	STB#	14	AFD#
2	PD0	15	ERR#
3	PD1	16	PINIT#
4	PD2	17	SLIN#
5	PD3	18	GND
6	PD4	19	GND
7	PD5	20	GND
8	PD6	21	GND
9	PD7	22	GND
10	ACK#	23	GND
11	BUSY	24	GND
12	PE	25	GND
13	SLCT	26	N.C

4. VGA connector (J21C)

- A. Connector size: 3 X 5 = 15 Pin
- B. 15 PIN D-SUB FEMALE

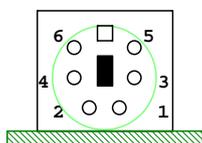


- C. Connector pin definition

Pin	Definition	Pin	Definition
1	RED	2	GREEN
3	BLUE	4	NC
5	GND	6	GND
7	GND	8	GND
9	KEY	10	GND
11	NC	12	ID1
13	HSYNC	14	HSYNC
15	ID3		

5. Key board connector (J21A)

- A. Connector size: Mini din 6 pins
- B. Connector location

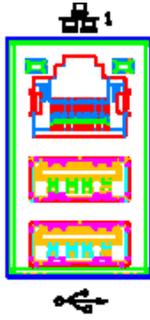


- C. Connector pin definition

Pin	Definition	Pin	Definition
1	KBDAT	2	MSDAT
3	GND	4	VCC
5	KBCLK	6	MSCLK

6. LAN connector (CON2/3/AB)

- A. Connector size: RJ-45&USB PORT
- B. Connector location:



C. LAN Connector pin definition:

Pin	Definition	Pin	Definition
1	BTX+	2	BTX-
3	BRX+	4	TERM PLANE
5	TERM PLANE	6	BRX-
7	TERM PLANE	8	TERM PLANE

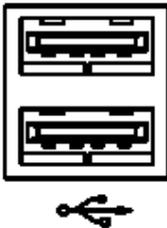
D. USB Connector pin definition

Pin	Definition	Pin	Definition
1	USB POWER +5V	2	USB PORT * D-
3	USB PORT * D+	4	GROUND POWER
5	USB POWER +5V	6	USB PORT * D-
7	USB PORT * D+	8	GROUND POWER

7. USB connector (CON1)

A. Connector size : USB PORT

B. Connector location:

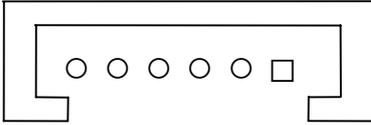


C. Connector pin definition

Pin	Definition	Pin	Definition
1	USB POWER +5V	2	USB PORT * D-
3	USB PORT * D+	4	GROUND POWER
5	USB POWER +5V	6	USB PORT * D-
7	USB PORT * D+	8	GROUND POWER

8. USB connector (J3)

- A. Connector size: 1 X 6 = JST 6PIN HEADER (2.00mm Pitch)
- B. Connector location



- C. Connector pin definition

Pin	Definition	Pin	Definition
1	USBVCC	2	USB1D-
3	USB1D+	4	USB2D-
5	USB2D+	6	USBGND

9. AUDIO CD- IN (J16)

- A. Connector size: 1 X 4 = 4 Pin Header (2.54mm Pitch)
- B. Connector location



1 2 3 4

- C. Connector pin definition

Pin	Definition	Pin	Definition
1	CD IN - L	2	AUDIO GROUND POWER
3	AUDIO GROUND POWER	4	CD IN - R

10. AUDIO LINE (CON4B)

Speaker out phone jack



Audio MIC in (CON4A) phone jack

11. POWER INPUT (JP6)

DC Adapter Board Power Input Connector

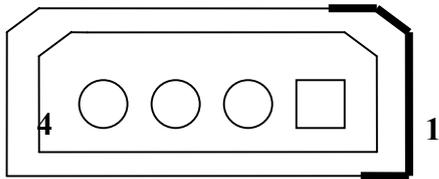


12. POWER OUTPUT (J6)

DC Adapter Board Power Output Connector

A. Connector size: 1X4= 4 Pin (3.96mm Pitch)

B. Connector location



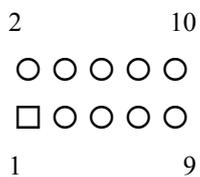
C. Connector pin definition

Pin	Name		Color	Description
1	+12V		Yellow	+12 VDC
2	GND		Black	Ground
3	GND		Black	Ground
4	+5V		Red	+5 VDC

13. GPIO connector (J15)

A. Connector size: 2 X 5 = 10 Pin Header (2.00mm Pitch)

B. Connector location



C. Connector pin definition

Pin	Definition	Pin	Definition
1	GP27IN (PIN20)	2	GP23OUT (PIN24)
3	GP26IN (PIN21)	4	GP22OUT (PIN25)
5	GP25IN (PIN22)	6	GP21OUT (PIN26)
7	GP24IN (PIN23)	8	GP20OUT (PIN27)
9	VCC5	10	GND

14. SMBUS connector (J17)

A. Connector size: 1 X 2 = 2 Pin Header (2.54mm Pitch)

B. Connector location



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C. Connector pin definition

Pin	Definition	Pin	Definition
1	SMBDATA	2	SMBCLK

15. FAN POWER connector (FAN1,FAN2,FAN3)

A. Connector size: 1 X 3 = 3PIN W/FAN (2.54mm Pitch)

B. Connector location



C. Connector pin definition

Pin	Definition	Pin	Definition
1	GND	2	+12V
3	Sensor		

16. POWER ON & IDE & LAN Link/Active LED connector (J1)

A. Connector size: 2 X 7 = 13 Pin Header (2.54mm Pitch)

B. Connector location

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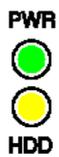


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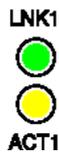
C. Connector pin definition

Pin	Definition	Pin	Definition
1	GND	2	ONLED
3	IDE Active	4	IDEACT#
5	SPEED_LAN1	6	LAN1_VCC
7	LILED_LAN1	8	ACTLED_LAN1
9	SPEED_LAN1	10	LAN2_VCC
11	LILED_LAN1	12	ACTLED_LAN2
13	NC		

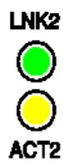
17. POWER ON & IDE Active LED (LED1)



18. LAN1 Link/Active LED (LED2)



LAN2 Link/Active LED (LED3)



19. Hardware Reset connector (J18)

A. Connector size: 1 X 2 = 2 Pin Header (2.54 Pitch)

B. Connector location

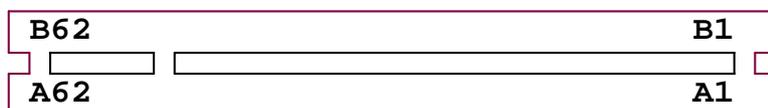


C. Connector pin definition

Pin	Definition	Pin	Definition
1	GND	2	Reset

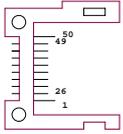
20. PCI1 - STANDARD PCI SLOT (J10)

STANDARD 5V / 32BIT / 33MHZ PCI SLOT



21. J8 – COMPACTFLASH SOCKET

(PRIMARY CHANNEL: DEFAULT MASTER)



22. TV-OUT (J21B)

A. Connector size: MINI DIN 7PIN

B. Connector location



C. Connector pin definition

Pin	Definition
1	VEDIO
2	GND
3	S-VEDIOC
4	GND
5	S-VEDIOV
6	GND

23. External thermal connector (J19)

A. Connector size: 1 X 2 = 2 Pin JST (2.5 Pitch)

B. Connector location

2 1

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C. Connector pin definition

Pin	Definition	Pin	Definition
1	GNDA	2	External thermal Input

24. IRDA connector (JP2)

A. Connector size: 1 X 5 = 5 Pin Header (2.54mm Pitch)

B. Connector location

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C. Connector pin definition

21	Ground	22	Write Disk Data
23	Ground	24	Write Gate
25	Ground	26	Track 0
27	Ground	28	Write Protected
29	Ground	30	Read Disk Data
31	Ground	32	Head Select
33	Ground	34	Disk Change

27. COM4 RI# or Power Status Select (JP3,JP4)

Pin	Status	Function Description
JP4 1-2	Short*	RI#
JP3 1-2	Short	Select +5V
JP3 2-3	Short	Select +12V

28. DVI (J4)

A. Connector size: 2 X 17 = 34 BOX Header (2.0mm Pitch)

B. Connector location

2 34

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C. Connector pin definition

PIN	Description	PIN	Description
1	Analog GND	2	Analog GND
3	T.M.D.S Data2-	4	T.M.D.S Data2+
5	GND	6	NC
7	NC	8	DDC Clock
9	DDC Data	10	NC
11	T.M.D.S Data1-	12	T.M.D.S Data1+
13	Analog GND	14	NC
15	NC	16	+5V
17	Analog GND	18	Hot Plug Detect
19	T.M.D.S Data0-	20	T.M.D.S Data0+
21	Analog GND	22	NC
23	NC	24	GND
25	T.M.D.S CLK+	26	T.M.D.S CLK-
27	GND	28	GND

29	NC	30	NC
31	NC	32	Analog GND
33	Analog GND	34	NC

29. ANEL VDD Select (J12)

Pin	Status	Function Description
1-3;2-4	Short	VCC5
3-5;4-6	Short*	VCC3

30. External -12V Input connector (J13)

A. Connector size: 1 X 2 = 2 Pin JST (2.5 Pitch)

B. Connector location

2 1

C. Connector pin definition

Pin	Definition	Pin	Definition
1	-12V	2	GND

31. LVDS connector (J20B)

A. Connector size: 3 X 15 = 44 Pin

B. 44 PIN D-SUB FEMALE



C. Connector pin definition

Pin	Definition	Pin	Definition	Pin	Definition
1	DDCPCLK	16	DDCPDATA	31	GND
2	CLKBM	17	LCKBP	32	GND
3	YBM3	18	YBP3	33	GND
4	YBM2	19	YBP2	34	GND
5	YBM1	20	YBP1	35	GND
6	YBM0	21	YBP0	36	GND
7	CLKAM	22	LCKAP	37	GND
8	YAM3	23	YAP3	38	GND
9	YAM2	24	YAP2	39	GND
10	YAM1	25	YAP1	40	GND
11	YAM0	26	YAP0	41	VDD(5V or 3.3V)
12	BKLTCTL	27	VR	42	VDD(5V or 3.3V)

13	VDD(5V or 3.3V)	28	VDD(5V or 3.3V)	43	BACKLIGHT
14	BACKLIGHT	29	BACKLIGHT	44	BACKLIGHT
15	BACKLIGHT	30	BACKLIGHT		

Note: BACKLIGHT is backlight inverter Power

Marked "*" was the default setting.

32. COM2 RS232/422/485 Select (SW1)

Mode	1	2	3	4	5	6	7	8	9	10
RS232*	OFF	OFF	OFF	ON	OFF	ON	OFF	OFF	OFF	OFF
RS422	OFF	OFF	ON	OFF	ON	OFF	ON	ON	ON	ON
RS485	ON	ON	OFF	ON	ON	OFF	OFF	OFF	OFF	ON

33. Pentium M FSB Select (J5)

Pin No.	Status	Function Description
1-3;2-4	Short*	400Mhz
3-5;4-6	Short	533Mhz

34. DC Adapter Input Voltage Select (JP7)

(On-board voltage 12V will be converted to 6V while JP7 select 1-2)

Pin No.	Status	Function Description
1-2	Short	8V-30V
2-3	Short*	14V-30V

35. CMOS Status Select (JP1)

Pin No.	Status	Function Description
1-2	Short*	Normal Operation
2-3	Short	Clear CMOS Data

Chapter 3

Expansion

3.1 System Memory

Your system memory is provided by DIMM's (Dual In-Line Memory Modules) on the CPU board. The board contains two memory banks: Bank 0 and 1, which correspond to connector DIMM1 and DIMM2. The table below shows possible DIMM Configurations for the memory banks. Please note that the NISE3100/EBC576 series supports Double Data Rate DDR 200/266/333 SDRAM. Configurations using different brands of memory modules are not recommended.

DIMM1	DIMM2	Total Memory
128 MB	Empty	128 MB
Empty	128 MB	128 MB
128 MB	128 MB	256 MB
256 MB	Empty	256 MB
Empty	256 MB	256 MB
256 MB	256 MB	512 MB
512 MB	Empty	512 MB
Empty	512 MB	512 MB
512 MB	512 MB	1024 MB
1024 MB	Empty	1024 MB
Empty	1024 MB	1024 MB
1024 MB	1024 MB	2048 MB

Table 3-1: DIMM Configurations of the NISE3100/EBC573 series

3.2 Installing DIMM

To install DIMM

1. Make sure the two handles of the DIMM sockets are in the “open” position, i.e. the handles stay outward.

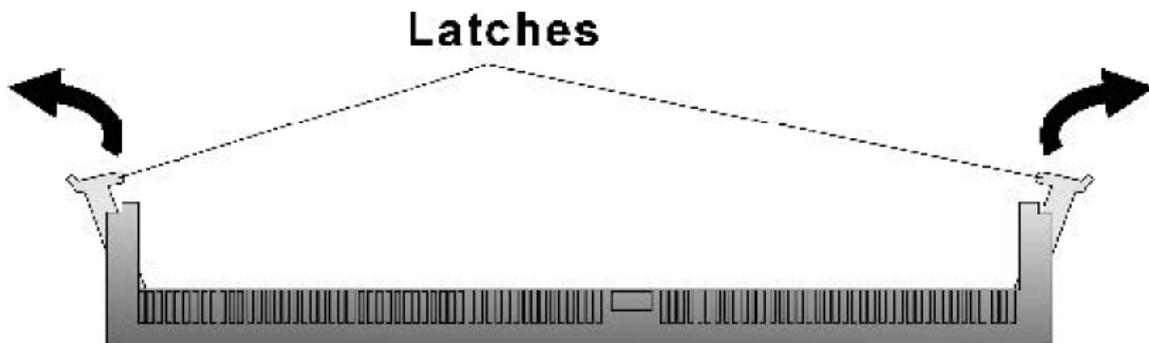


Figure3-1: How to Install DIMM (1)

2. Slowly slide the DIMM modules along the plastic guides in the both ends of the socket.

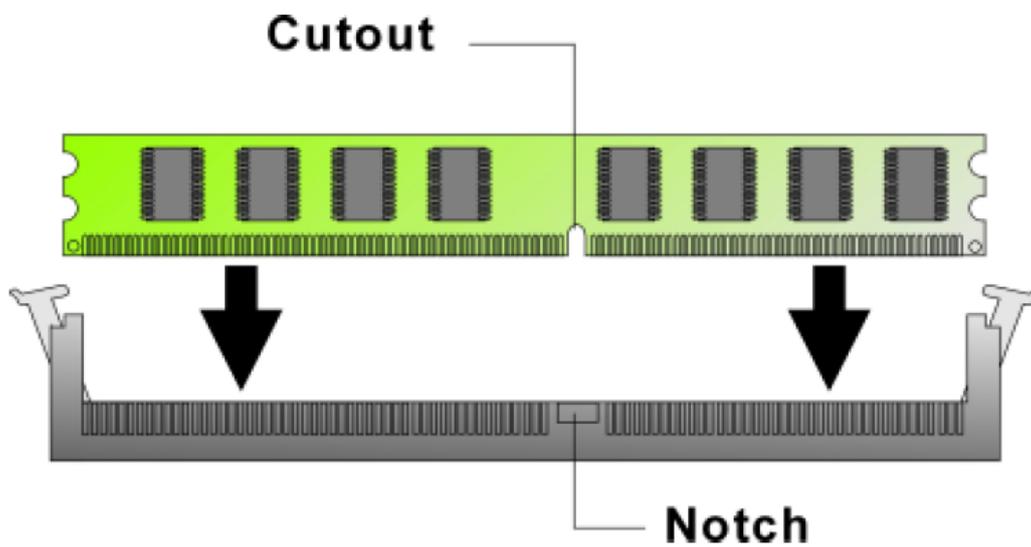


Figure 3-2: How to Install DIMM (2)

3. Then press the DIMM module down right into the socket, until a click is heard. That means the two handles automatically locked the memory modules into the right position of the DIMM socket.

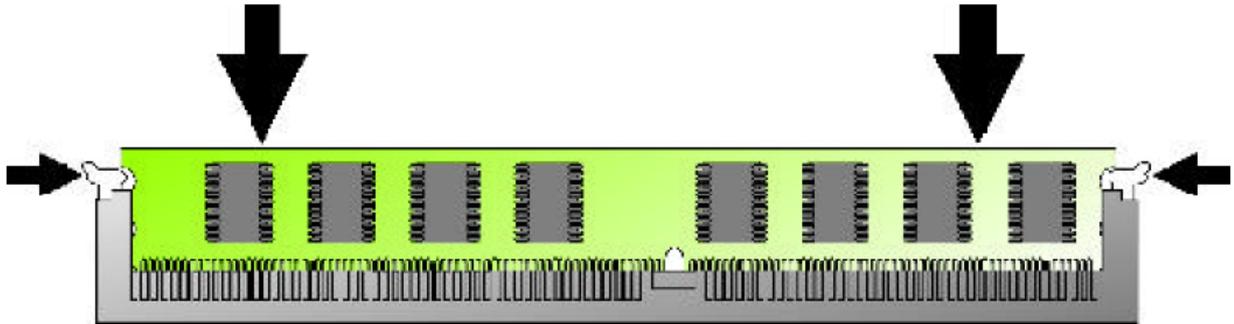


Figure 3-3: How to Install DIMM (3)

4. To take away the memory module, just push the both handles outward, the memory module will be ejected by the mechanism in the socket.

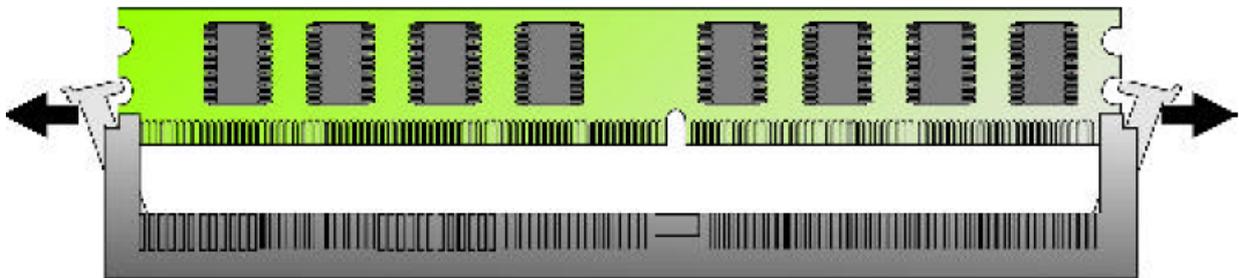


Figure 3-4: How to Install DIMM (4)

3.3 Installing Compact Flash

1. To install a Compact Flash memory card into EBC573, align the notches on the card with the Compact Flash socket in the EBC573. Then firmly insert the card into the socket until it is completely seated.

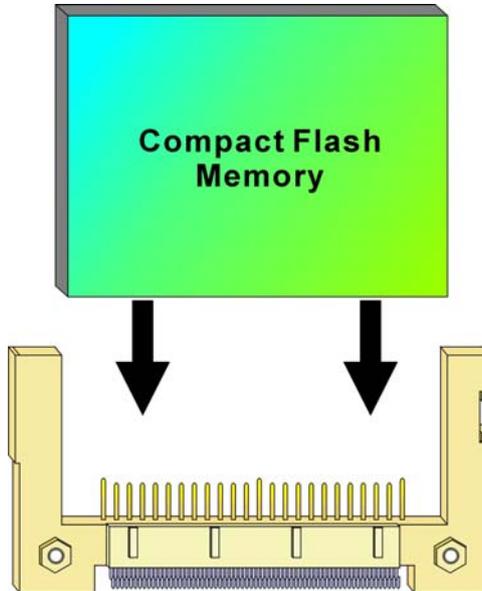


Figure 3-5: How to Install Compact Flash Memory (1)

2. To remove the Compact Flash memory card from EBC573, pull out the memory card from the Compact Flash socket.

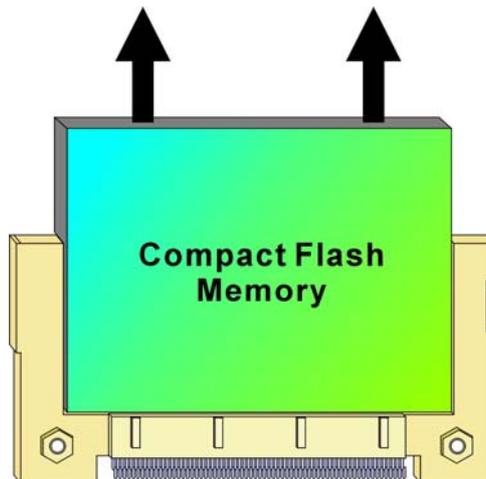


Figure 3-6: How to Uninstall Compact Flash Memory (2)

3.4 Installing Intel Pentium-M CPU and Heatsink

Below is the installation instruction:

Note: Prepare a slot type screwdriver before starting the installation process.

Step1: Be sure that the beveled corner of the CPU as shown in the picture is aligned with that of the socket.

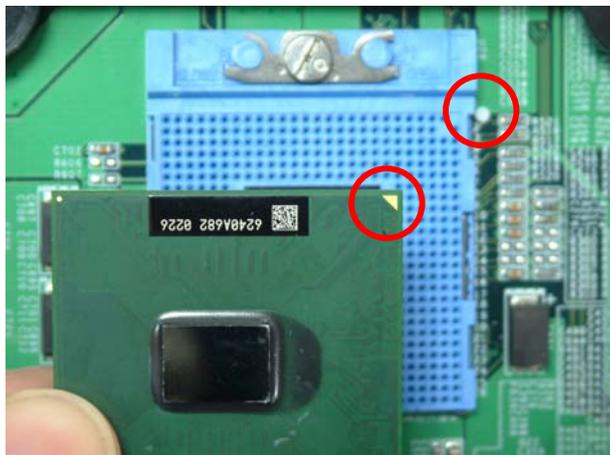


Figure 3-7: How to Install CPU (1)

Step2: Screw it tight as shown in the picture.

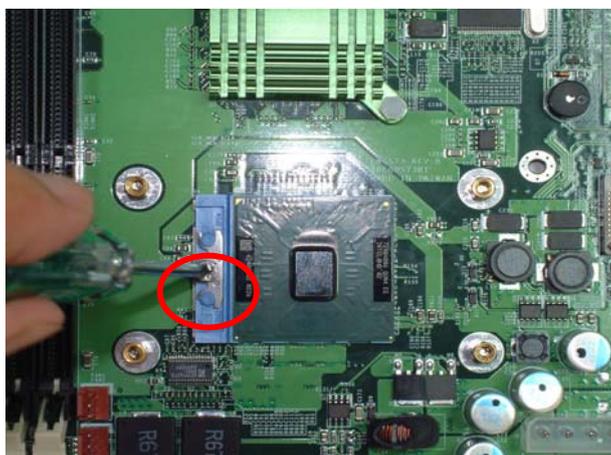


Figure 3-8: How to Install CPU (2)

Step3: Apply the heatsink ointment to the location on the CPU as indicated by the arrow.



Figure 3-9: How to Install CPU (3)

Step4: Attach the heatsink onto the CPU. The exhaust opening should face the Northbridge. Connect the power connector to the jumper and tighten the four screws as shown.

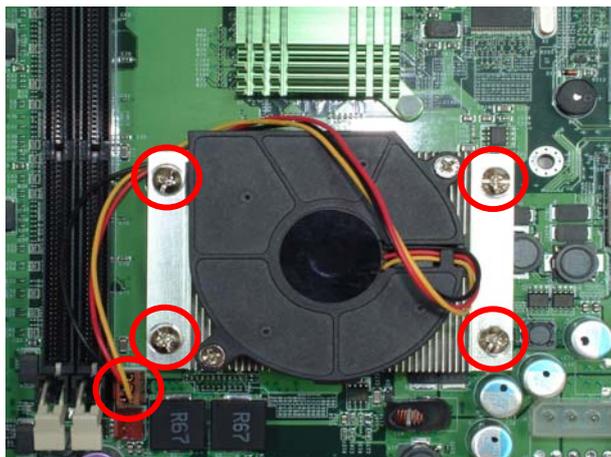


Figure 3-10: How to Install fan heatsink (1)

Chapter 4

Award BIOS Setup

This chapter explains how to use the BIOS Setup program for the EBC573. The current BIOS setup pictures in the chapter are for reference only, which may change by the BIOS modification in the future. User can download any major updated items or reversion from NEXCOM web site <http://www.nexcom.com.tw>. If any unclear message occurs, please contact NEXCOM customer service representative for help or log onto <http://www.nexcom.com.tw/contact/contact.htm>.

4.1 About the BIOS

The BIOS (Basic Input and Output System) Setup program is a menu driven utility that enables you to make changes to the system configuration and tailor your system to suit your individual work needs. It is a ROM-based configuration utility that displays the system's configuration status and provides you with a tool to set system parameters. These parameters are stored in non-volatile battery-backed-up CMOS RAM that saves this information even when the power is turned off. When the system is turned back on, the system is configured with the values found in CMOS.

With easy-to-use pull down menus, you can configure such items as:

- ◆ Hard drives, diskette drives, and peripherals
- ◆ Video display type and display options
- ◆ Password protection from unauthorized use
- ◆ Power management features

The settings made in the Setup program intimately affect how the computer performs. It is important, therefore, first to try to understand all the Setup options, and second, to make settings appropriate for the way you use the computer.

4.2 When to Run BIOS

This program should be executed under the following conditions:

- ◆ When changing the system configuration
- ◆ When a configuration error is detected by the system and you are prompted to make changes to the Setup program
- ◆ When resetting the system clock
- ◆ When redefining the communication ports to prevent any conflicts
- ◆ When making changes to the Power Management configuration
- ◆ When changing the password or making other changes to the security setup

Normally, CMOS setup is needed when the system hardware is not consistent with the information contained in the CMOS RAM, whenever the CMOS RAM has lost power, or the system features need to be changed.

Standard CMOS Features

Use this menu for basic system configuration.

Advanced BIOS Features

Use this menu to set the Advanced Features available on the system.

Advanced Chipset Features

Use this menu to change the values in the chipset registers and optimize the system's performance.

Integrated Peripherals

Use this menu to specify your settings for integrated peripherals.

Power Management Setup

Use this menu to specify your settings for power management.

PnP/PCI Configurations

This entry appears if your system supports Plug and Play and PCI Configuration.

PC Health Status

Displays CPU, System Temperature, Fan Speed, and System Voltages Value.

Load Fail-Safe Defaults

Use this menu to load the BIOS default values for the minimal/stable performance for your system to operate.

Load Optimized Defaults

Use this menu to load the BIOS default values, i.e., factory settings for optimal performance system operations. While Award has designed the custom BIOS to maximize performance, the factory has the option to change these defaults to meet their needs.

Set Password

Enables you to change, set, or disable the supervisor or user password.

Save & Exit Setup

Saves CMOS value changes to CMOS and exits setup

Exit Without Saving

Ignores all CMOS value changes and exits setup.

4.5 Getting Help

Main Menu

The on-line description of the highlighted setup function is displayed at the bottom of the screen.

Status Page Setup Menu/Option Page Setup Menu

4.6 Control Keys

The table below lists the keys that help you navigate the setup program.

Up arrow		Move to previous item
Down arrow		Move to next item
Left arrow		Move to the item to the left
Right arrow		Move to the item to the right
Esc key		Main Menu: Quit without saving changes to CMOS Status/Option Page Setup Menus: Exit current page and return to Main Menu.
Enter Key		Select or Accept an Item
PgUp/plus key		Increase the numeric value or make changes
PgDn/minus key		Decrease the numeric value or make changes
F1 key		General help, only for Status Page Setup Menu and Option Page Setup Menu
F2/Shift + F2 key		Change color from total 16 colors. F2 to select color forward, (Shift) F2 to select color backward
F5 key		Restore the previous CMOS value from CMOS (only for Option Page Setup Menu)
F6 key		Load the default CMOS value from BIOS default table (only for Option Page Setup Menu)
F7 key		Load the Setup default value (only for Option Page Setup Menu)
F9 Key		Menu in BIOS
F10 key		Save all the CMOS changes (only for Main Menu)

4.7 Standard CMOS Features

Selecting Standard CMOS Features on the main program screen displays the following menu:

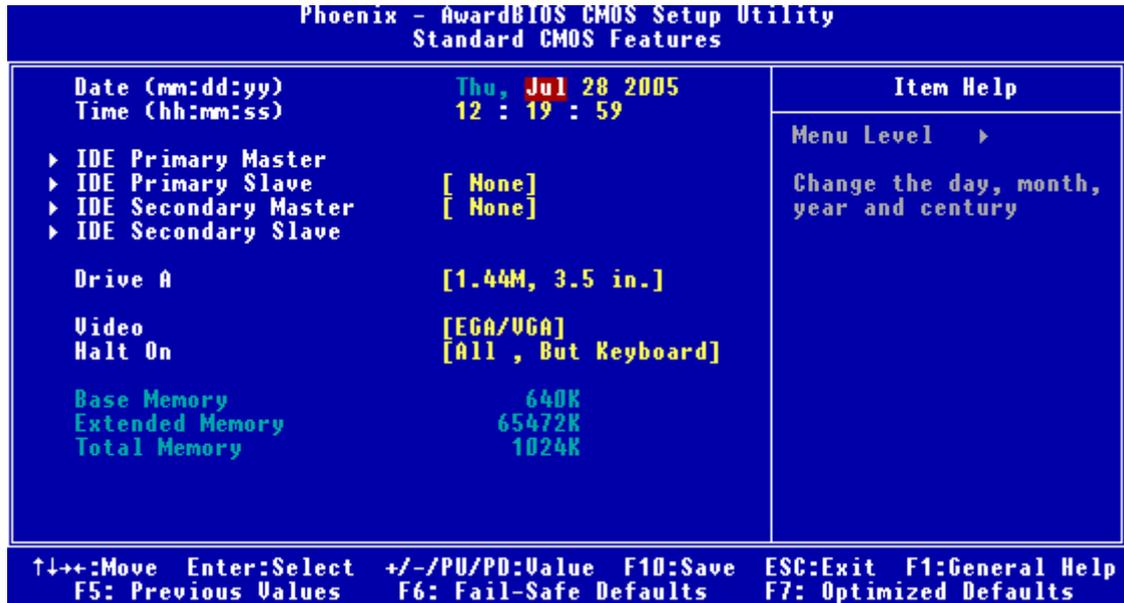


Figure 4-2: BIOS – Standard CMOS Features

The Standard CMOS Setup utility is used to configure the following features:

Date (mm:dd:yy)

The BIOS determines the day of the week from the other data information. This field is for information only. Press the left or right arrow key to move to the desired field (date, month, year). Press the PgUp or PgDn key to increment the setting, or type the desired value into the field.

Time (hh:mm:ss)

The time format is based on the 24-hour military time clock. For example, 1 p.m. is 13:00:00. Press the left or right arrow key to move to the desired field. Press the PgUp or Pg Dn key to increment the setting, or type the desired value into the field.

IDE Devices (Primary/Secondary Master/Slave)

Your computer has two IDE channels (Primary and Secondary) and each channel can be installed with one or two devices (Master and Slave). Use these items to configure each device on the IDE channel. If you leave this item at Auto, the system will automatically detect and configure any IDE devices it finds. If it fails to find a hard disk, change the value to Manual and then manually configure the drive by entering the characteristics of the drive in the items below:

- ◆ Capacity Approximate hard disk drive capacity
- ◆ Cylinder Number of cylinders
- ◆ Head Number of heads
- ◆ Precomp Write pre-compensation cylinder

- ◆ Landing Zone Landing zone
- ◆ Sector Number of sector

Refer to your drive's documentation or look on the drive if you need to obtain this information. If no device is installed, change the value to None.

Drive A

Select this field to the type of floppy disk drive installed in your system. The choices are:

- ◆ None: No floppy drive installed
- ◆ 360K, 5.25 in: 5-1/4 inch PC type standard drive; 360 kilobyte capacity
- ◆ 1.2M, 5.25 in: 5-1/4 inch AT-type high-density drive; 1.2 megabyte capacity
- ◆ 720K, 3.5 in: 3-1/2 inch double-sided drive; 720 kilobyte capacity
- ◆ 1.44M, 3.5 in: 3-1/2 inch double-sided drive; 1.44 megabyte capacity
- ◆ 2.88M, 3.5 in: 3-1/2 inch double-sided drive; 2.88 megabyte capacity

Note: The None option could be used for diskless workstations.

Video

Set this field to the type of graphics card installed in your system. If you are using a BGA or higher resolution card, choose the EGA/VGA option. The options are:

- ◆ EGA/VGA Enhanced Graphics Adapter/Video Graphics Array. For EGA, VGA, SEGA or PGA monitor adapters
- ◆ CGA40 Color Graphics Adapter, power up in 40 column mode
- ◆ CGA80 Color Graphics Adapter, power up in 80 column mode
- ◆ MONO Monochrome adapter, includes high resolution monochrome adapters

Halt On

During the Power-On Self-Test (POST), the computer stops if the BIOS detect a hardware error. This setting determines which type of error will cause the system to halt during boot. The options are:

- ◆ All Error: Whenever the BIOS detects a non-fatal error, the system will be stopped and you will be prompted.
- ◆ No Errors: The system boot will not stop for any error that may be detected.
- ◆ All, But Keyboard: The system boot will not stop for a keyboard error, but it will stop for all others.

After you have made your selections in the Standard CMOS Setup screen, press <ESC> to go back to the main screen.

4.8 Advanced BIOS Features

Selecting Advanced BIOS Feature on the main program screen displays this menu, which allows you to define advanced information about your system. You can make modifications to most of these items to improve your system performance or set up system features according to your preference, without causing fatal errors to your system.

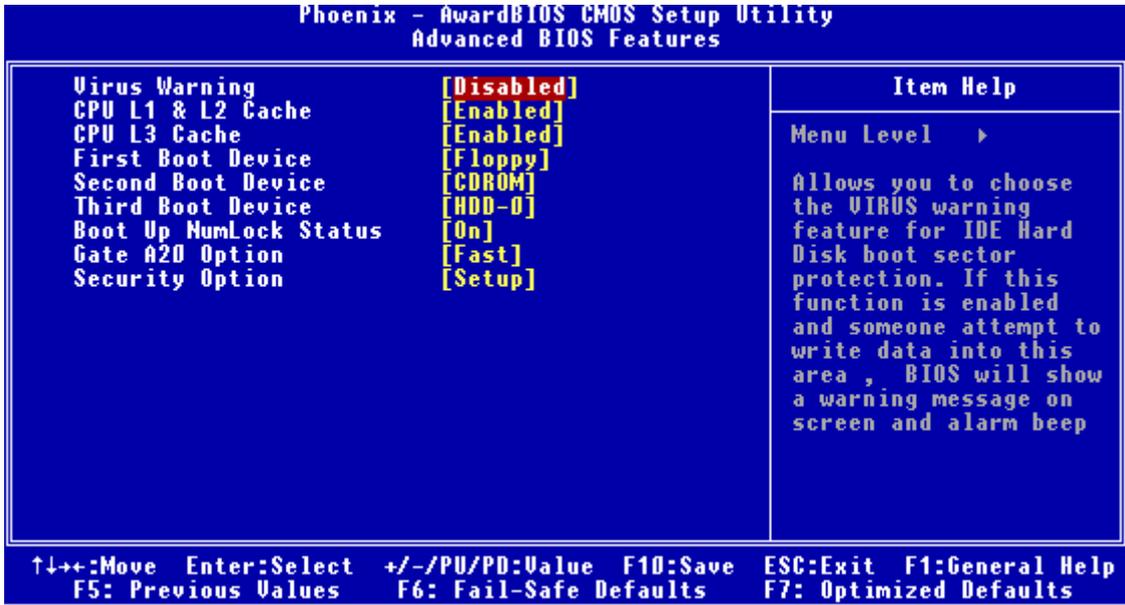


Figure 4-3: BIOS – Advanced BIOS Features

The following explains the options for each feature:

Virus Warning

Allow you to choose the Virus Warning feature for IDE Hard Disk boot sector protection. If this function is enabled and someone attempts to write data into this area, BIOS will show a warning message on screen and an alarm will beep.

- ◆ Enabled: Activates automatically when the system boots up causing the following warning message to appear when anything attempts to access the boot sector or hard disk partition table:

WARNING!

Disk boot sector is to be modified

Type "Y" to accept write or "N" to abort write Award

Software, Inc.

- ◆ Disabled: No warning message will appear when an attempt is made to access the boot sector or hard disk partition table.

Note: This function is available only for DOS and other operating systems that do not trap INT13. For complete protection against viruses, install virus software in your operating system and update the virus definitions regularly.

Many disk diagnostic programs that access the boot sector table can trigger the virus warning message. If you plan to run such a program, we recommend that you disable the virus warning.

CPU L1, L2 and L3 Cache

Cache memory is an additional memory that is much faster than conventional DRAM (system memory).

This BIOS feature is used to enable or disable the processor's Level 1, Level 2 and Level 3 cache. Naturally, the default and recommended setting is Enabled.

Note: This field will be available only if your CPU supports this function.

First/Second/Third Boot Device

BIOS attempts to load the operating system from the devices in the sequence selected. The available choices are: Floppy, HDD-0, SCSI, CDROM, HDD-1, HDD-2, HDD-3, USB-FDD, USBZIP, USB-CDROM, USB-HDD, LAN, and Disabled.

Boot Up NumLock Status

Toggle between On or Off to control the state of the NumLock key when the system boot. If On, the numeric keypad is in numeric mode. If Off, the numeric keypad is in cursor control mode.

Gate A20 Option

Gate A20 refers to the way the system addresses memory above 1MB (extended memory). This feature enables you to select whether the chipset or the keyboard controller should control Gate A20. The options are:

- ◆ **Normal:** A pin in the keyboard controller controls Gate A20
- ◆ **Fast** : Let system chipsets control Gate A20. The fast setting improves system speed, particularly with OS/2 and windows.

Security Option

Enables you to select whether the password is required every time the system boots or only when you enter Setup.

- ◆ **System:** The system will not boot and access to Setup will be denied if the correct password is not entered at the prompt.
- ◆ **Setup:** The system will boot, but access to Setup will be denied if the correct password is not entered at setup.

4.9 Advanced Chipset Features

Since the features in this section are related to the chipset in the CPU board and all are optimized, you are not recommended to change the default settings in the setup table, unless you understand the chipset features.

This section allows you to configure the system based on the specific features of the installed chipset.

This chipset manage bus speeds and access to system memory resources, such as DRAM and the external cache. It also coordinates communications between the conventional ISA bus and the PCI bus. The default settings have been chosen because they provide the best operating conditions for your system. The only time you might consider making any changes would be if you discovered that data was being lost while using your system.

The first chipset settings deal with CPU access to dynamic random access memory (DRAM). The default timings have been carefully chosen and should only be altered if data is being lost. Such a scenario might well occur if your system has mixed speed DRAM chips installed so that greater delays may be required to preserve the integrity of the data held in the slower memory chips.

Selecting Advanced Chipset Features on the main program screen displays this menu:

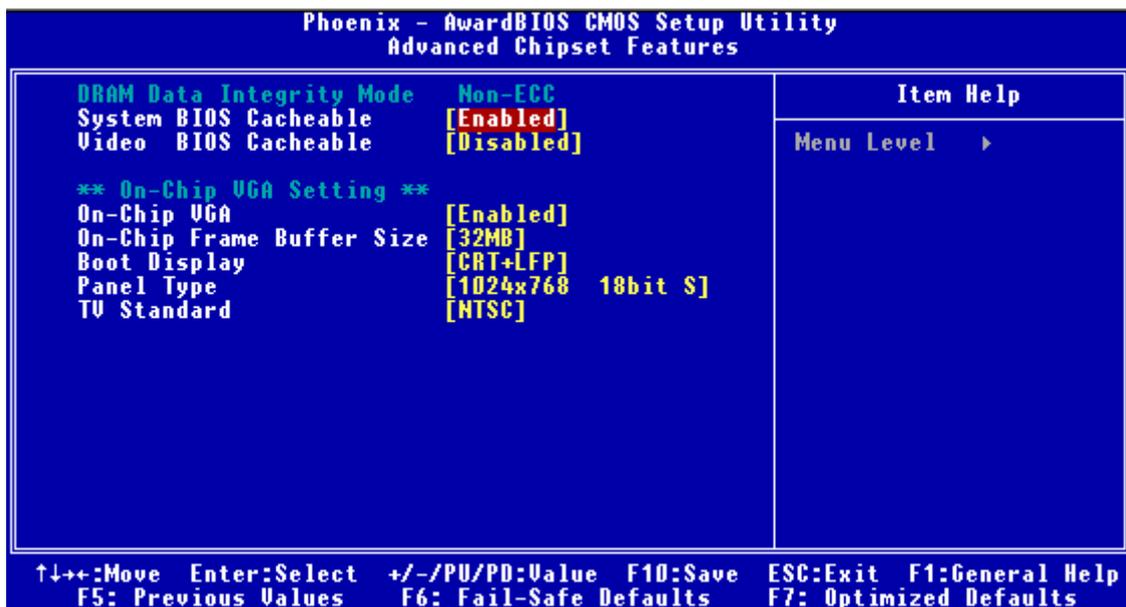


Figure 4-4: BIOS – Advanced Chipset Features

System BIOS cacheable

Selecting Enabled allows caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result. The available choices are Enabled, Disabled.

Video BIOS Cacheable

Selecting Enabled allows caching of the video BIOS ROM at C0000h, resulting in better video performance. However, if any program writes to this memory area, a system error may result. The choices: Enabled, Disabled.

On-Chip VGA

By default, the On-Chip VGA or chipset-integrated VGA is Enabled.

On-Chip Frame Buffer Size

The On-Chip Frame Buffer Size can be set as 1, 4, 8, 16 or 32MB. This memory is shared with the system memory.

Boot Display

Boot Display determines the display output device where the system boots. The options are Auto, CRT, LFP, and CRT+LFP.

Panel Type

This field allows user to decide the LVDS panel resolution. Please refer to the BIOS for the resolution. After you have made your selections in the Advanced Chipset Features setup, press <ESC> to go back to the main screen. The options are 640x480 18bits, 800x600 18bits, 1024x768 18bits.

TV Standard

This item allows you to designate the type of colored TV standard to be used when a TV receiver is connecting to the TV out port. If a TV receiver is not connected to the XL2, this setting should be disabled. NTSC is for U.S. colored TVs; PAL is for European and other non-U.S. TVs.

4.10 Integrated Peripherals

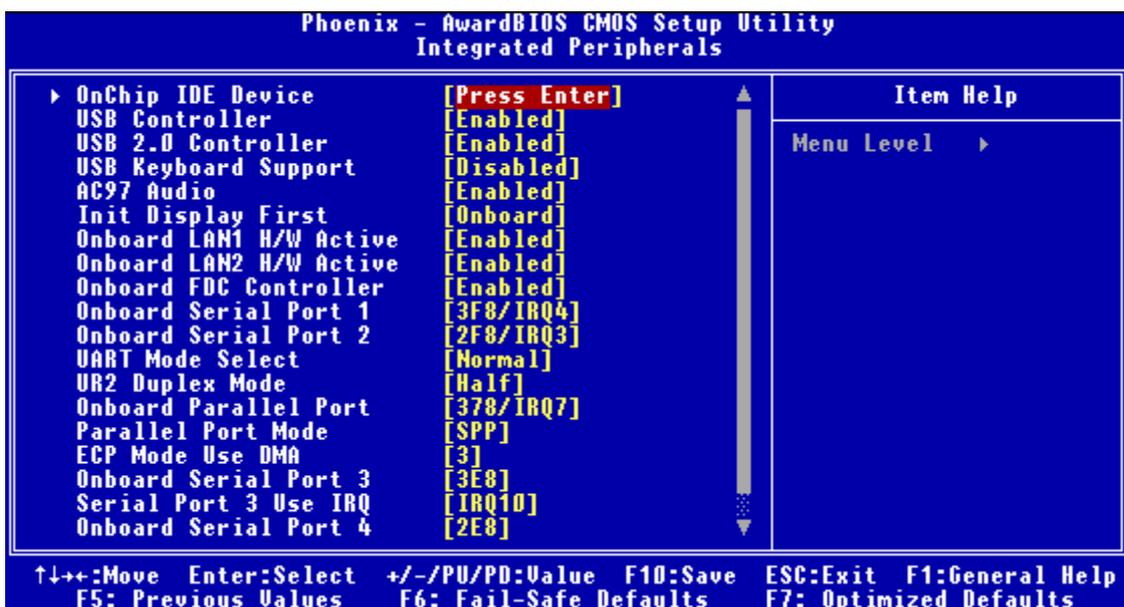


Figure 4-5: BIOS – Integrated Peripherals

OnChip IDE Device

Select this item to setup the IDE device features. When you select this item, the following menu shows:

USB 2.0 Controller

Select Enabled if your system contains a Universal Serial Bus 2.0 controller and you have USB 2.0 peripherals.

USB Keyboard Support

Select Enabled if your USB controller is enabled and it needs USB keyboard support in legacy (old) OS operating systems such as DOS.

AC97 Audio

Selecting Auto will enable the AC'97 audio if it is detected onboard.

Init Display First

This feature allows you to select whether to boot the system using the onboard AGP graphics card or the PCI graphics card.

Onboard LAN 1/2 H/W Active

Enables and disables the onboard LAN modules.

Onboard FDC Controller

Select Enabled if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install an add-in FDC or the system has no floppy drive, select Disabled to this field.

Onboard Serial Ports (1, 2, 3, 4)

This feature allows you to manually select the I/O address and IRQ for the first and second serial ports. It is recommended that you leave it as Auto so that the BIOS can select the best settings for it. But if you need a particular I/O port or IRQ that's been taken up by this serial port, you can manually select an alternative I/O port or IRQ for it. You can also disable this serial port if you do not need to use it. Doing so frees up the I/O port and IRQ used by this serial port. Those resources can then be reallocated for other devices to use.

UART Mode Select

Select an operating mode for the serial port.

The choices are: Normal, IrDA, ASKIR.

UR2 Duplex Mode

In an infrared port mode, this field appears. Full-duplex mode permits simultaneous two-direction transmission. Half-duplex mode permits transmission in one direction only at a time. Select the value required by the IR device connected to the IR port.

Onboard Parallel Port

This feature allows you to select the I/O address and IRQ for the onboard parallel port. The default I/O address of 378h and IRQ of 7 should work well in most cases. Unless you have a problem with the parallel port, you should leave it at the default settings. The choices: 378/IRQ7, 278/IRQ5, 3BC/IRQ7, and Disabled.

Parallel Port Mode

Select an operating mode for the onboard parallel (printer) port. There are four options: SPP (Standard Parallel Port), EPP (Enhanced Parallel Port), ECP (Extended Capabilities Port) and ECP+EPP.

ECP Mode Use DMA

When the on-board parallel port is set to ECP mode, the parallel port can use DMA3 or DMA1.

Serial Port 3\4 Use IRQ

This assigns an IRQ to the serial port 3\4.

4.11 Power Management Setup

This option lets you control system power management. The system has various power-saving modes including powering down the hard disk, turning off the video, suspending to RAM, and software power down that allows the system to be automatically resumed by certain events.

The power-saving modes can be controlled by timeouts. If the system is inactive for a time, the timeouts begin counting. If the inactivity continues so that the timeout period elapses, the system enters a power saving mode. If any item in the list of Reload Global Timer Events is enabled, then any activity on that item will reset the timeout counters to zero.

If the system is suspended or has been powered down by software, it can be resumed by a wake up call that is generated by incoming traffic to a modem, a LAN card, a PCI card, or a fixed alarm on the system realtime clock. Selecting Power Management Setup on the main program screen displays this menu:

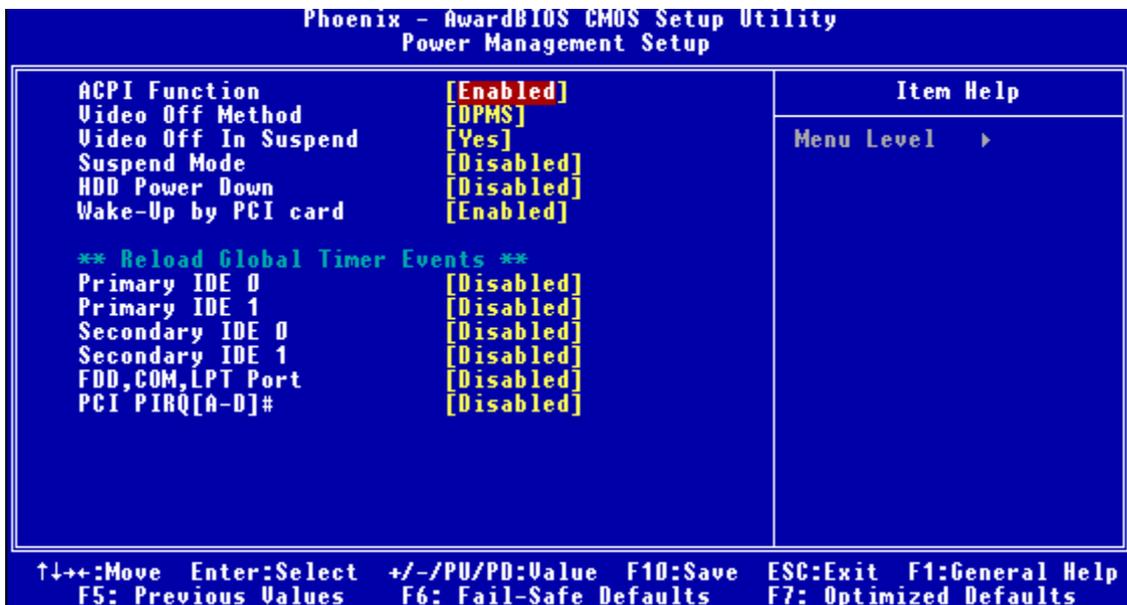


Figure 4-6: BIOS – Power Management Setup

ACPI Function

The ACPI standard (Advanced Configuration and Interface power) allows the operating system directly to check the functions of energy saving and the PnP (Plug and Play) functionality. The ACPI functions are normally activated by the BIOS. The choices are: Enabled and Disabled.

Video Off Method

This determines the manner in which the monitor is blanked. There are three choices:

1. V/H SYNC+Blank: This selection will cause the system to turn off the vertical and horizontal synchronization port and write blanks to the video buffer.
2. Blank Screen: This option only writes blanks to the video buffer.
3. DPMS Support: Select this option if your monitor supports the Display Power Management signaling (DPMS) standard of the Video Electronics Standard to select video power management values.

Video Off In Suspend

This determines the manner in which the monitor is blanked. The choices: Yes, No.

Suspend Mode

After the selected period of system inactivity, all devices except the CPU shut off. The choices are 1~2 min, 2~3 min,.... Up to 1 hour.

HDD Power Down

After the selected period of drive inactivity, the hard disk drive powers down while all other devices remain active.

Wake up by PCI Card

When the system enters a Soft-off mode (Standby power exists but system is not working), it will wake up system when specific signals occurred. The BIOS monitors the system for "activity" to determine when to enable power management.

If you enable this feature, the computer specifies that any signal noticed on the PCI (Peripheral Component Interconnect) bus channel must make go out from the hibernation state. The choices: Enabled, Disabled.

Reload Global Timer Events

Primary/Secondary IDE 0/1

FDD, COM, LPT Port

PCI PIRQ [A-D]#

The events are I/O events whose occurrence can prevent the system from entering a power saving mode or can awaken the system from such a mode. In effect, the system remains alert for anything, which occurs to a device, which is configured as Enabled, even when the system is in a power down mode. The choices are Enabled, and Disabled.

After you have made your selections in the Power Management setup, press the <ESC> key to go back to the main program screen.

4.12 PnP/PCI Configurations

This section describes configuring the PCI bus system. Peripheral Component Interface, is a system which allows I/O devices to operate at speeds nearing the speed the CPU itself uses when communicating with its own special components. This section covers some very technical items and it is strongly recommended that only experienced users should make any changes to the default settings.

Selecting PnP/PCI Configurations on the main program screen displays this menu:

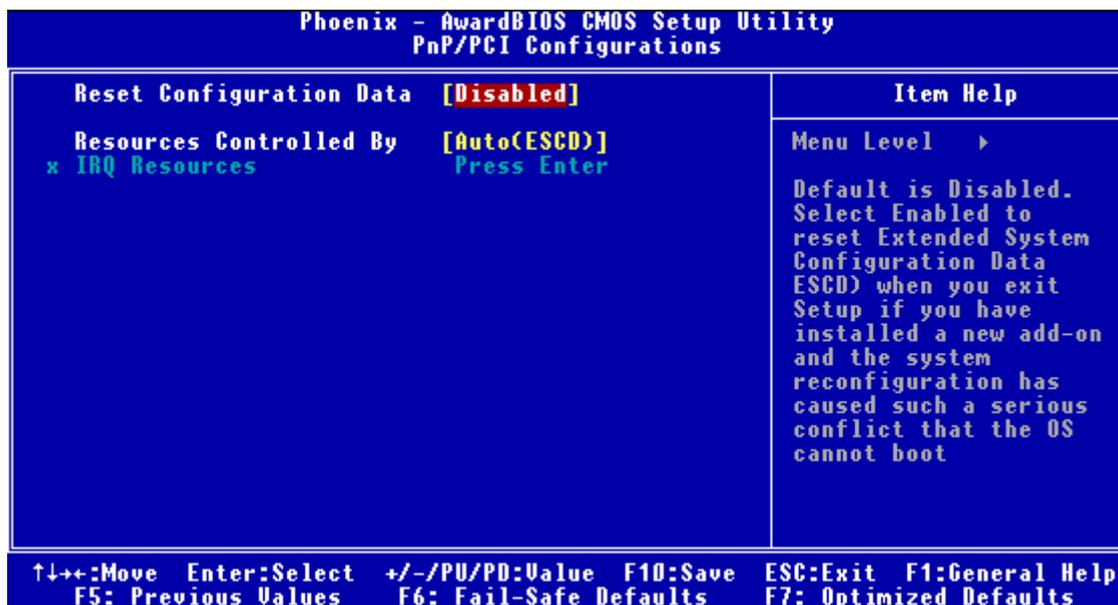


Figure 4-7: BIOS – PnP/PCI Configurations

Reset Configuration Data

Normally, you leave this field Disabled, Select Enabled to reset Extended System Configuration Data (ESCD) when you exit Setup if you have installed a new add-on Card and the system reconfiguration has caused such a serious conflict that the operating system can not boot. The choices are Enabled and Disabled.

Resources Controlled By

The Award Plug and Play BIOS has the capacity to automatically configure all of the boot and Plug and Play compatible devices. However, this capability means absolutely nothing unless you are using a Plug and Play operating system such as going into each of the submenus that follows this field. The choices are Auto (ESCD), Manual.

4.13 PC Health Status

When main boards support hardware monitoring, this item lets you monitor the parameters for critical voltages, critical temperatures, and fan speeds. These are the read only items.

After you have read the PC Health Status, press the <ESC> key to go back to the main program screen.

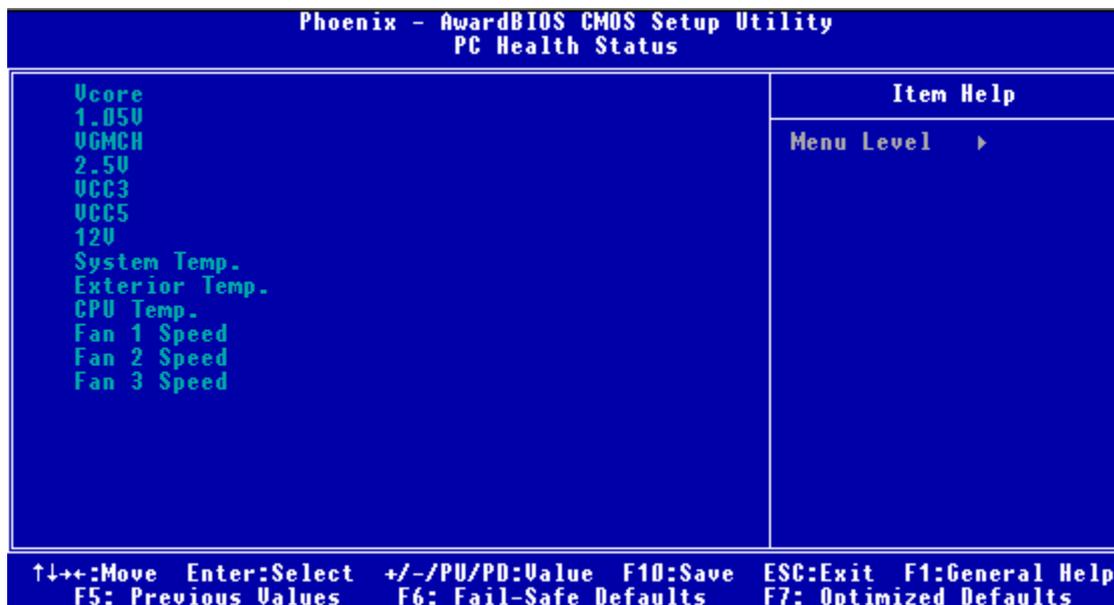


Figure 4-8: PC Health Status

4.14 Load Fail-Safe Defaults

This option opens a dialog box that lets you install fail-safe defaults for all appropriate items in the whole setup utility. Press the <Y> key and then <Enter> to install the defaults. Press the <N> key and then <Enter> to not install the defaults.

Use this option if you have changed your system and it does not operate correctly or does not power up.

4.15 Load Optimized Defaults

This option opens a dialog box that lets you install optimized defaults for all appropriate items in the whole setup utility. Press the <Y> key and then <Enter> to install the defaults. Press the <N> key and then <Enter> to not install the defaults. The optimized defaults place demands on the system that may be greater than the performance level of the components, such as the CPU and the memory. You can cause fatal errors or instability if you install the optimized defaults when your hardware does not support them. If you only want to install setup defaults for a specific option, select and display that option, and then press the <F7> key.

4.16 Set Password

The User Password utility sets the password. The main board is shipped with the password disabled. If you want to change the password, you must first enter the current password, then at the prompt enter your new password. The password is case sensitive. You can use up to eight alphanumeric characters. Press <Enter> after entering the password. At the next prompt, confirm the new password by retyping it and pressing <Enter> again.

To disable the password dialog box appears. A message appears confirming that the password has been disabled. If you have set supervisor and user Password, only the supervisor password allows you to enter the BIOS setup program.

Note: If you forget your password, the only way to solve this problem is to discharge the CMOS memory by turning power off and placing a shunt (jumper cap) on jumper JP2 to short pin 2 and pin 3 for five seconds, then putting the shunt back to pin 1 and pin 2 of JP2.

4.17 Save & Exit Setup

Selecting this option and pressing <Enter> will save the new setting information in the CMOS memory and continue with the booting process.

4.18 Exit Without Saving

Selecting this option and pressing <Enter> will exit the Setup utility without recording any new values or changing old ones.

Appendix A

Watchdog Timer

The NISE3100/EBC573 series features a watchdog timer that reset the CPU or generates an interrupt if the processor stops operating for any reason. This feature ensures system reliability in industrial standalone or unmanned environments.

A.1 Watchdog Timer Working Procedure

The Watchdog Timer (WDT) is a special hardware device that monitors the computer system during normal operation. The WDT has a clock circuit that times down from a set number to zero. If a monitored item occurs before the timer reaches zero, the WDT resets and counts down again. If for some reason the monitored item doesn't occur before the timer reaches zero, the WDT performs an action, such as a diagnostic operation (rebooting the computer) or generate an NMI.

You must enter timer values into the WDT Configuration Register (Write the control value to the Configuration Port), and clear (read the Configuration Port).

WDT Configuration port	I/O port 2E0h	Read/Writable, default at 2E0h
Watchdog Timer	Disabled	Disable WDT functions (Default setting)
	Enabled	Enable WDT functions controlled by WDT time out active for and WDT Time Out Active Time
WDT Time out active for	Reset Output	Reset system when WDT time out
	NMI Output	Generate NMI when WDT time out
	WDT Notice Output	1. Located at J8 pin No. 6 2. Normal work output low level 3. Output high level when WDT time-out, read or write WDT configuration port return to normal work (output low level)
WDT Time Out Active Time	1 sec/min 2 sec/min 4 sec/min 8 sec/min 16 sec/min 32 sec/min 64 sec/min 128 sec/min	WDT time out occurs after the selected time level

Table A-1: Watchdog Timer Character and Function

A.2 Watchdog Timer Control Register

The EBC573/NISE3100 features a watchdog timer that reset the CPU or generates an interrupt if the processor stops operating for any reason. This feature ensures system reliability in industrial standalone or unmanned environments. The Watchdog Timer (WDT) is a special hardware device that monitors the computer system during normal operation.

GPIO Configuration Registers:

LDN	Index	R/W	Reset	Configuration Register or Action
07h	71h	R/W	00h	Watch Dog Timer Control Register
07h	72h	R/W	00h	Watch Dog Timer Configuration Register
07h	73h	R/W	00h	Watch Dog Timer Time-out Value Register

Panel Button De-bounce Base Address LSB Register (Index=65h, Default=00h)

Bit	Description
7-0	Read/Write, mapped as Base Address [7:0]

Panel Button De-bounce Interrupt Level Select Register (Index=70h, Default=00h)

Bit	Description
7-4	Reserved
3-0	Select the interrupt level (note1) for Panel Button De-bounce

Watchdog Timer Control Register (Index=71h, Default=00h)

Bit	Description
7	WDT is reset upon a CIR interrupt
6	WDT is reset upon a KBC (mouse) interrupt
5	WDT is reset upon a KBC (keyboard) interrupt
4	WDT is reset upon a read or a write to the Game Port base address
3-2	Reserved
1	Force Time-out. This bit is self-clearing
0	WDT Status
	1: WDT value reaches 0
	0: WDT value is not 0

Watchdog Timer Configuration Register (Index=72h, Default=00h)

Bit	Description
7	WDT Time-out value select
	1: Second
	0: Minute
6	WDT output through KRST (pulse) enable
	1: Enable

	0: Disable
5-4	Reserved
3-0	Select the interrupt level for WDT
1	Force Time-out. This bit is self-clearing

Watchdog Timer Time-out Value Register (Index=73h, Default=00h)

Bit	Description
7-0	WDT time-out value 7-0]

Sample Code:

```
;Enter config mode
```

```
out 2E, 87h
```

```
out 2E, 01h
```

```
out 2E, 55h
```

```
out 2E, 55h
```

```
;Set LDN=7
```

```
out 2E, 07h
```

```
out 2F, 07h
```

```
;Set WDT enable, second mode
```

```
out 2E, 72h
```

```
out 2F 0C0h
```

```
;Set value=3
```

```
out 2E, 73h
```

```
out 2F, 03h
```

Power on or reset the system

The initial value of WDT Control Register (D4~D0) is zero, when power is on or reset the system. Following shows the initial value of WDT (00000000b):

Bit	Value	Meaning
7	0	Disable WDT
6	0	Reset output is disable
5	0	NMI output is disable
4	0	WDT Notice output is disable
3	0	Select WDT count mode by second
2, 0,1	000	Select time-out occurs after 1 second/minute

Table A-2: WDT Control Register Initial Value

Clear the WDT

The WDT counter internal cannot be longer than the preset time; otherwise, the WDT generates a NMI (Non Maskable Interrupt) or sends a reset signal to the system.

Note: Before running WDT, clear the WDT to make sure the initial value is zero before enabling the WDT.

WDT Control Register (Write to WDT configuration port)

Note: This register writes to the WDT configuration port.

You can set the WDT Control Register to control the WDT working mode.

Follow below instructions to set the initial value of the WDT working mode.

1. Select the WDT time out occurs time

Time-out intervals decide by values of bit 2, bit 1, bit 0 in I/O port 2E0h minute or second decide by values of bit 3 in I/O port 2E0h

2. Enable or Disable WDT Notice Output decide by bit 4 value in I/O port 2E0h
3. Enable or Disable NMI Output decide by bit 5 value in I/O port 2E0h
4. Enable or Disable Reset Output decide by bit 6 value in I/O port 2E0h
5. Enable or Disable the WDT decide by bit 7 value in I/O port 2E0h

After finishing the above settings, you must output the Control Register's value to the WDT Configuration Port. Then WDT will start according to the above settings.

Note: Build a mechanism in the program to continue to read the WDT Configuration Port for clearing WDT before time out

Appendix B

GPI/O Programming

B.1 GPIO Programming

GPIO (General Purpose Input/Output) pins are provided for custom system design. This appendix provides definitions and its default setting for the ten GPIO pins in the NISE3100/EBC573 series. The pin definition is shown in the following table:

Pin No.	GPIO mode	PowerOn Default	Address	Pin No.	GPIO mode	PowerOn Default	Address
1	GPI	Read High	801H (0)	2	GPO	High	801H (4)
3	GPI	Read High	801H (1)	4	GPO	High	801H (5)
5	GPI	Read High	801H (2)	6	GPO	High	801H (6)
7	GPI	Read High	801H (3)	8	GPO	High	801H (7)
9	VCC	-	-	10	GND	-	-

Table B-1: J15 – GPIO Connector

Read the GPI Pin (1/3/5/7) status form I/O port 801H bit (0/1/2/3).

The bit is Set/Clear indicated High/Low

Control the GPO pin (2/3/6/8) level from I/O port 801H bit (4/5/6/7).

The bit is Set/Clear indicated output High/Low

Note: All of these GPIO pins are 8mA digital open-drain buffer and internal pull-up.

Appendix C

Installation Guide

C.1 Handling Precautions

1. Always disconnect the unit from the power outlet whenever you are installing or fixing a component inside the chassis.
2. If possible, always wear a grounded wrist strap when you are installing or fixing a component inside the chassis. Alternatively, discharge any static electricity by touching the bare metal chassis of the unit case, or the bare metal body of any other grounded appliance.
3. Hold electronic circuit boards (such as the EBC575 board) by the edges only. Do not touch the components on the board unless it is necessary to do so. Do not flex or stress the circuit board.
4. Use the correct screws and do not overly tighten them.
5. Keep the original packaging and static-protective bag in case the unit has to be returned.

C.2 Packing List

Assemblages

- | | |
|--|--|
| * EBC573FL(LF)-AX embedded CPU board x 1 | * EBK PCI101 1 Slot PCI Riser Card x 1 |
| * NICE3100 chassis set x 1 | * IDE Cable x 1 |
| | * DVI cable x 1 |

Accessories

- | | |
|--------------------------------|------------------------------------|
| * Quick Reference Guide x 1 | * 2-pin Phoenix power connector x1 |
| * PS2 keyboard/mouse cable x 1 | * CD Driver x 1 |
| * COM Port Cable x 1 | * 120W Power Adapter x 1 |

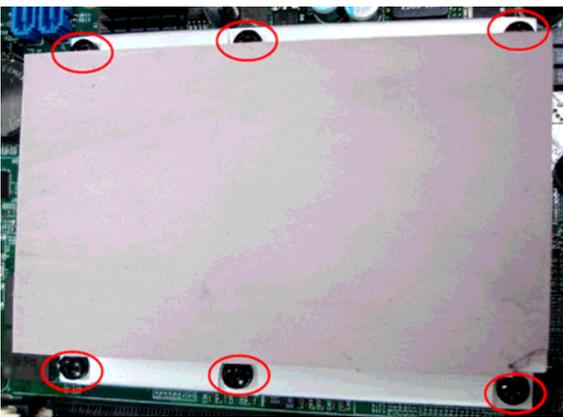
C.3 Installation

Open Top Cover



Step 1: Remove 6 screws from the top.

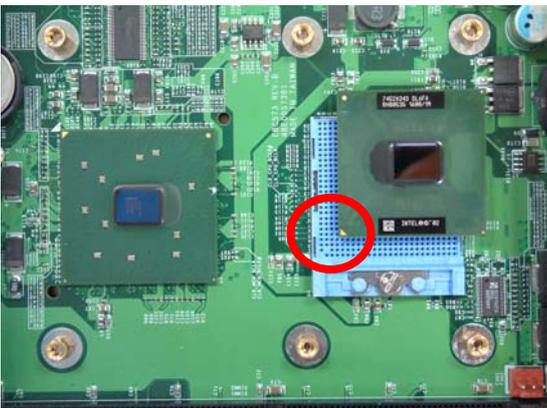
Install/Remove CPU



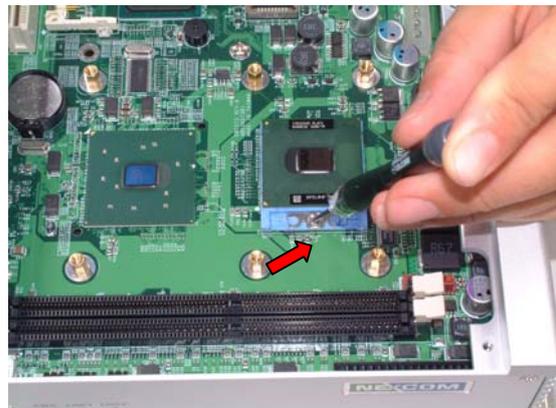
Step 2: Unscrew 6 screws on heat sink



Step 2.1: Pay attentions to CPU installation



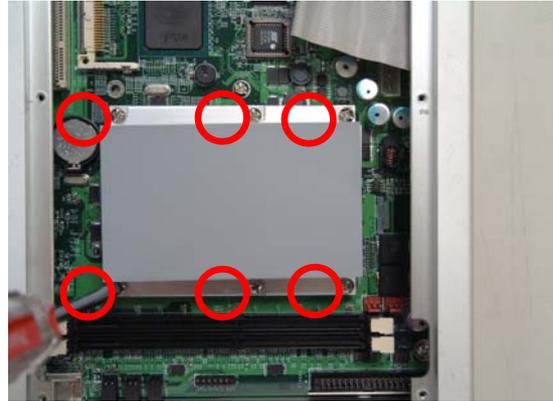
Step 2.2: Be aware that the beveled corner of the CPU as shown in the picture is aligned to the direction of the socket.



Step 2.3: Secure the CPU



Step 2.4: Insert heat sink silicon compound with screw



Step 2.5: Lock the heat sink

Install/Remove RAM module



Step 3: Insert either 1 or 2 DDR

Close Top Cover



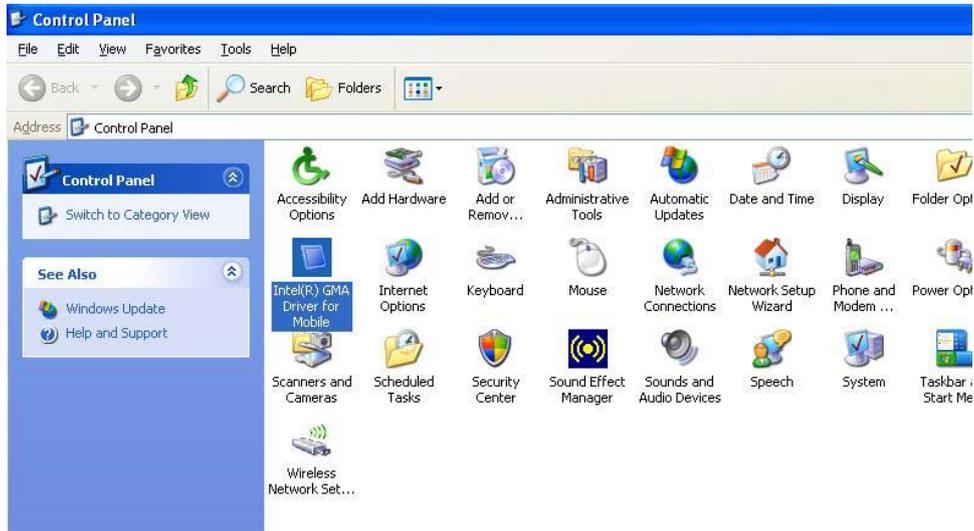
Step 4: Secure the top cover with screws

Appendix D

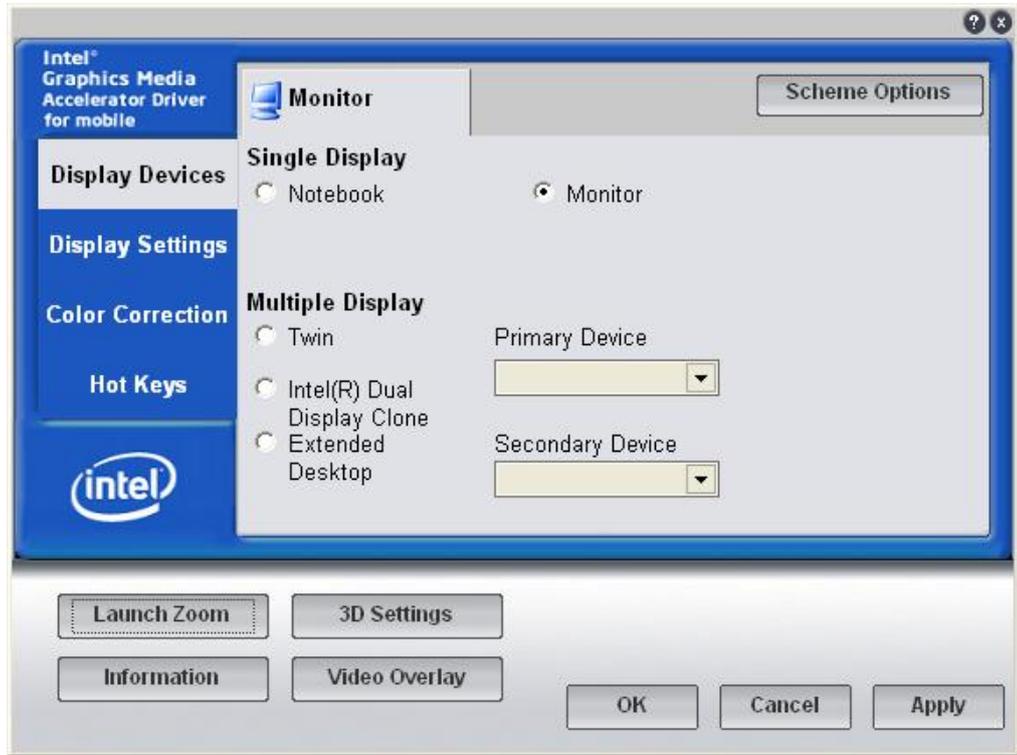
Display setting

Since the chipset belongs to Intel mobile group, LVDS is the default of display. Once VGA port is not linked with monitor when power on the NISE system, LVDS will be the first display automatically afterwards. Follow below steps to select the VGA/DVI monitor as the default display for your need.

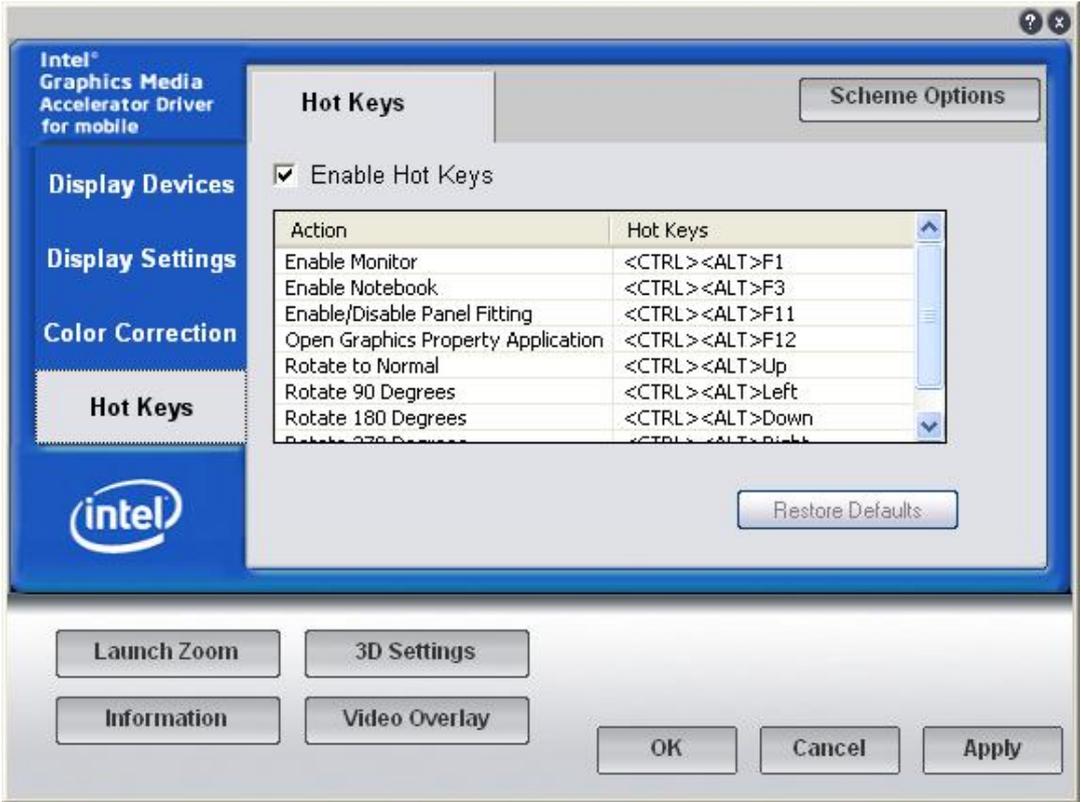
- 1. Have NISE system linked with VGA/DVI monitor
- 2. Select "Intel GMA Driver for Mobile" from Control Panel



- 3. Select "Monitor" from Display Devices



4. Select "Enable Hot Keys" from Hot Keys



After setting, press "CTRL + ALT + F1" to enable monitor as the main display if have the same situation.